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Report 11190  
July 1998

**GENCORP**  
**AEROJET**

**Integrated Advanced Microwave Sounding Unit-A  
(AMSU-A)**

**Performance Verification Report**

**Subassembly and Complete Instrument Assembly**

**METSAT AMSU-A2 Antenna Drive Subassembly,**

**P/N 1331200-2, S/N 105**

**Contract No. NAS 5-32314  
CDRL 208**

**Submitted to:**

**National Aeronautics and Space Administration  
Goddard Space Flight Center  
Greenbelt, Maryland 20771**

**Submitted by:**

**Aerojet  
1100 West Hollyvale Street  
Azusa, California 91702**

**Aerojet**

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## AMSU-A VERIFICATION TEST REPORT

TEST ITEM:	METSAT AMSU- A2 ANTENNA DRIVE SUBSYSTEM PART OF P/N: 1331200-2 SERIAL NUMBER: 105
LEVEL OF ASSEMBLY:	SUBASSEMBLY AND COMPLETE INSTRUMENT ASSEMBLY
TYPE HARDWARE:	FLIGHT
VERIFICATION: PROCEDURE NO.	AE-26002/2C
TEST DATE:	
ASSEMBLIES:	START DATE: 12 May 1998
SUBSYSTEM:	START DATE: 17 June 1998

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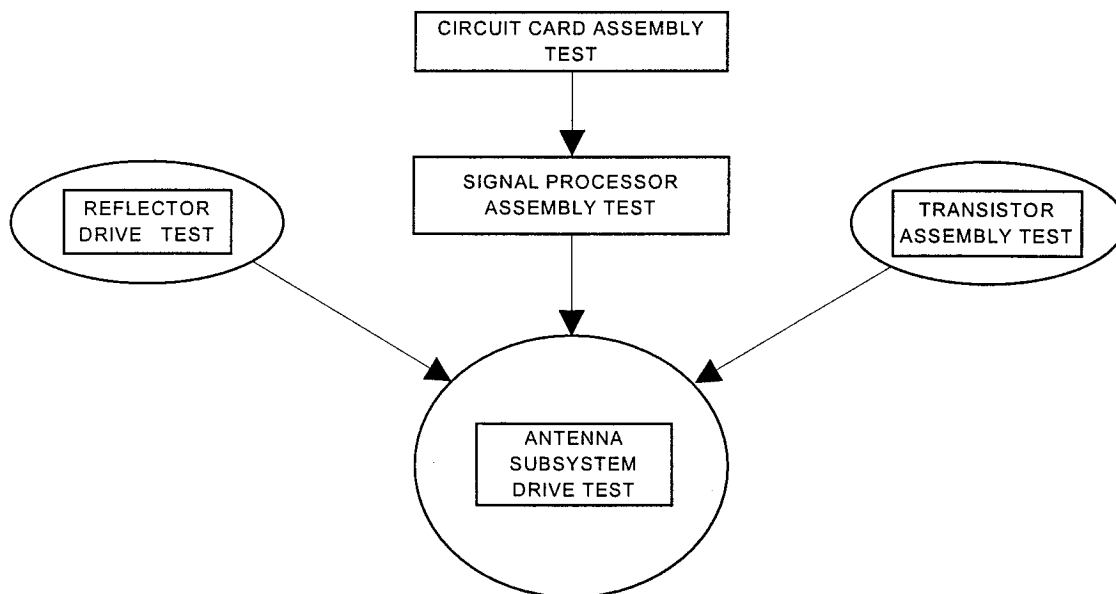
## 1.0 INTRODUCTION

An antenna drive subsystem test was performed on the METSAT AMSU-A2 S/N 105 instrument. The objective of the test was to demonstrate compliance with applicable paragraphs of AMSU-A specifications S-480-80. Tests were conducted at both the subassembly and instrument level.

## 2.0 SUMMARY

The antenna drive subsystem of the METSAT AMSU-A2 S/N 105, P/N 1331200-2, completed acceptance testing per AES Test Procedure AE-26002/2C. The test included: Scan Motion and Jitter, Noisy Bus Peak Current and Rise Time, Resolver Reading and Position Error, Gain/ Phase Margin, and Operational Gain Margin.

The drive motor and electronic circuitry were also tested at the component level. The drive motor test includes: Starting Torque Test, Motor Commutation Test, Resolver Operation/ No-Load Speed Test, and Random Vibration. The electronic circuitry was tested at the Circuit Card Assembly (CCA) level of production; each test exercised all circuit functions. The transistor assembly was tested during the W3 cable assembly (1356946-1) test. Refer to Figure 1 for test flow.



Antenna Subsystem and Subsystem Component Test Flow  
Figure 1.

The antenna drive subsystem satisfactorily passed all of the performance requirements. There were no failures in any of the antenna drive components during subsystem testing.

The results of the subsystem and component level testing are discussed in more detail in the following sections:

Reflector Drive Assembly.....	5.1
Circuit Card Assemblies .....	5.2
Signal Processor.....	5.3
Transistor Assembly .....	5.4
Antenna Drive Subsystem.....	5.5

### 3.0 TEST CONFIGURATION

The *Reflector Drive Assembly Tests* confirm the operability of the motor under test. The test configuration includes, the motor, motor shaft, bearings, and a supporting housing.

The *Circuit Card Assembly (CCA) Tests* confirm the operability of each CCA. Each test includes the CCA under test, electronic test fixtures, and the necessary loads.

A segment of the *Signal Processor Tests* ensures the scan drive electronics are functioning properly prior to it's assembly into the instrument. The test configuration includes:

- Timing and Control CCA
- Scan Control Interface CCA
- Relay Driver and Current Monitor CCA
- Interface Converter CCA
- Resolver Data Isolator CCA
- R/D Converter CCA
- Motor Driver CCA
- Test fixture and cabling to simulate the spacecraft bus interface
- Test fixture and cabling to interrogate and analyze positional data
- Test motor and inertia wheel

The *Transistor Assembly Test* verifies the correct wiring of the transistor assembly and associated cabling. Test configuration includes the CKT 1000 (continuity and Hi-Pot tester), and test fixtures.

#### The Antenna Drive Subsystem Tests:

- Are configured with the same motor control CCA's used in the signal processor test, interconnecting wiring, the power transistor assembly, and the drive assembly with reflector.
- The antenna drive subsystem components were all installed in the instrument when the subsystem test was performed.
- DC power for the motor control circuit cards was provided by a DC/DC converter simulator P/N: 1359322-1. The simulator operates on 120VAC facility supplied power. The power for the reflector motor drive circuits however was provided directly by the STE 28V Bus power supply.

## 4.0 TEST SETUP

The antenna drive subsystem tests are performed during system integration. During system integration testing, the instrument is proven electrically safe via ground isolation, and power distribution checks. Next, the communication link is exercised to ensure commands are received and interpreted correctly. The Antenna Drive Subsystem Test is then performed.

## 5.0 TEST RESULTS

The Antenna Drive Subsystem components designated for use in the METSAT AMSU-A2 S/N 105 instrument are shown in Table 1. During preliminary testing of these components (in preparation for the antenna drive subsystem test), several component failures occurred. The component failures and system related dispositions are listed below:

- **Compensator Drive Motor** - failed motor current limit at -10°C plateau; both CW and CCW. The specification was modified, with customer approval, to accommodate the out of tolerance condition.
- **Transistor Assembly** - failed continuity tests due to incorrect wiring. The assembly was re-wired and passed all subsequent tests

CCA	S/N
Resolver Data Isolator Assembly	F28
Interface Converter Assembly	F23
Scan Motor Driver Assembly	F09
Compensator Driver Assembly	F06
R/D Converter/ Oscillator Assembly	F09

OTHER	S/N
Reflector Drive Motor	F02
Compensator Drive Motor	F06
Signal Processor	F01
Transistor Assembly (W3 cable)	N/A

TABLE 1  
METSAT AMSU-A2 S/N 105 Antenna Subsystem Component S/N Designations

All other components designated for use in the METSAT AMSU-A2 instrument (pertaining to the scan drive circuitry) passed on the first time through component testing.

## 5.1 ANTENNA AND COMPENSATOR DRIVE ASSEMBLY

The tests performed on these units are: Starting Torque Test, Motor Commutation Test, Resolver Operation/ No-Load Speed Test, and Random Vibration. The Motor Commutation and Resolver Operation tests are performed both pre and post-vibration.

### Starting Torque

The starting torque test is performed on the rotating segment of the drive assembly to verify the torque associated with bearing friction. Both the reflector drive assembly (F02) and the compensator drive assembly (F06) passed the starting torque test at ambient temperature as well as at the colder plateaus.

### Motor Commutation Test

This test is performed to determine the commutation characteristics of the motor under test. The reflector drive assembly (F02) passed the motor commutation test both pre- and post-vibration tests without incident. The compensator drive assembly (F06) failed the current limit requirement at the -10°C plateau. Relief from the specification requirement was requested for and granted by the customer via FRB.

### Resolver Operation/ No-Load Speed Test

This test is performed to verify resolver operation as well as speed characteristics and back electromotive force of the motor. Both the reflector drive assembly (F02) and the compensator drive assembly (F06) passed the resolver operation/ no-load speed test both pre- and post-vibration tests without incident.



### Random Vibration

Vibration testing was successfully completed; both motors passed the vibration requirements without incident. Both the reflector drive assembly (F02) and the compensator drive assembly (F06) passed the pre- and post-vibration electronic tests as well as the post-vibration visual inspection without incident.

## **5.2 CIRCUIT CARD ASSEMBLIES**

Test procedures were prepared for each motor control circuit card; document revision status is controlled by reference in the shop order. The cards were individually tested to the procedures and results were recorded on data sheets found in Appendix A. The following list indexes the CCA Test Data Sheets:

- *Appendix A1 ..... Resolver Data Isolator Assembly*
- *Appendix A2 ..... Interface Converter Assembly*
- *Appendix A3 ..... Motor Driver Assembly*
- *Appendix A4 ..... R/D Converter/ Oscillator Assembly*

All circuit card assemblies passed testing the first time through. The assembly build shop orders contain the part number and accept tag record the of test and select resistors.

## **5.3 SIGNAL PROCESSOR**

For the first time, the entire antenna drive motor electronics is mated together. The test instrumentation commands and interrogates the electronics during this segment of testing. The instrumentation sends position commands to the Interface Converter CCA. The Interface Converter D/A's the command and provides inputs to the Motor Driver CCA. The test motor (instrumentation) responds to the drive signal and feeds back positional data via resolver outputs. The instrumentation then interrogates the Resolver Data Isolator CCA for position data. A comparison is made in the instrumentation between the position command sent and the actual position received. The pass/ fail indication is presented to the operator for test data sheet recording.

The signal processor assembly (F01) passed all scan drive tests.

## 5.4 TRANSISTOR ASSEMBLY

All transistor assemblies are tested along with their respective W3 cable. The cable is continuity, then hi-pot tested prior to attaching the transistor circuitry. Each transistor pair is exercised validating the turn on voltage, current drawn, and cable wiring as well.

During continuity testing, prior to applying power to the transistor assembly, it was noted that the transistors were wired improperly. The assembly was rewired in accordance with the corrected planning. Tests results were positive; all components operated as designed.

## 5.5 ANTENNA SUBSYSTEM DRIVE TESTS

The antenna drive motor electronics mates with the instrument microprocessor for the first time during this segment of testing. The microprocessor sends position commands from the memory CCA to the Interface Converter CCA. The Interface Converter D/A's the command and provides inputs to the Motor Driver CCA. The Reflector Drive Motor responds to the drive signals and feeds back positional data via the resolver outputs. The microprocessor then interrogates the Resolver Data Isolator CCA for position data.. The microprocessor in turn communicates with the spacecraft interface.

During other segments of the test, positional data is monitored via a potentiometer attached to the shaft of the reflector drive assembly. This provides scan characteristic information in regard to overshoot, jitter, and beam position transition timing.

The remaining paragraphs in this section discuss tests that ensures the instrument complies with specific operating parameters. Prior to conducting these tests there is a series of preliminary checks that are run to select component values that customize the operating parameters of the instrument. These checks perform the following functions:

- Program "on board" memory with Beam Position Pointing Angles
- Adjust for peak Motor Current Limits
- Observe Preliminary Scan Dynamics
- Identify Mechanical Resonant Frequencies

**Beam Position Pointing Angles** are calculated from Nadir pointing direction which is determined on the antenna range. The instrument's EPROMs (EPROMs for testing; PROMs for final configuration) are programmed to reflect the position commands. The initial programming may require fine tuning; fine tuning is determined during the remaining segments of the test procedure.

**Motor Current Limits** were adjusted, via selecting "test and select" resistors, to comply with the specification requirement; less than 2 amp peak current.

**Preliminary Scan Dynamics** looked good; transition times, overshoot and jitter were all acceptable at the sampled pointing directions (5).

The *Mechanical Resonant Frequencies* were identified; notch filters were calculated and installed to compensate for these resonant frequencies.

### 5.5.1 SCAN MOTION AND JITTER

In this test, the antenna position was measured in a series of five 8-sec full scans. The measurement was made with a 1-turn test potentiometer temporarily affixed to the rear end of the motor shaft. A Dynamic Signal Analyzer (DSA) was connected to the pot wiper to record the antenna position data. Five scans were captured and stored on the AMSU-A2 Test Data File disc. One representative waveform is presented in Appendix B1.

Each 3.33 degrees scene step was expanded and checked for a 42 msec max step time, and the 158 msec integration period. Expanded waveforms were plotted and are presented in Appendix B2 thru B30. All of the scene steps meet the step response requirement for transition time, overshoot, and jitter.

Slew periods to the cold and warm calibration stations were measured and met requirements. A time of 0.21 sec is allocated for the 35.0 degree slew to cold cal, and 0.40 sec for the 96.67 degree slew to warm cal. Calibration station jitter was less than the  $\pm 5\%$  maximum permitted. Expanded waveforms were plotted and are presented in Appendix B31 thru B34. The waveforms are also stored on the AMSU-A2 Test Data File disc. The test data sheet is presented in Appendix B35

### 5.5.2 NOISY BUS PEAK CURRENT AND RISE TIME

The noisy pulse load bus peak current and the rate of change of current were measured. The peak current must be less than 2A at any beam position along the scan. Peak current along the scan is 1.88A. The current rate of change while transitioning from one beam position to the next (including the transition to the cold calibration and warm calibration targets) should be greater than 70 microseconds. A random 3.33° step was selected; the transition to the next step was 1.1 ms. The transition to the warm cal position start and stop was significantly longer than the required 70 ms; 1.6 and 56 ms respectively.

The peak bus current was measured across the entire scan and met the requirement. The full scan waveform was plotted and is presented in Appendix C1. The waveform is also stored on the AMSU-A2 Test Data File disc. The test data sheet is presented in Appendix C2

### 5.5.3 RESOLVER READING AND POSITION ERROR

The 14-bit command position word is stored in the "on-board" memory and is read to the motor drive circuitry under microprocessor program control. The microprocessor also

reads the resolver output at each of the thirty scene stations, and at the cold and warm calibration positions. The readings are made at the start of integration (LOOK 1), and halfway into the integration period (LOOK 2). The resolver data is sent to the spacecraft interface for subsequent transmission to the STE.

The purpose of this portion of the test is to demonstrate that the antenna is meeting beam pointing requirements.

If the antenna is out of the pointing tolerance of  $> \pm 10$  counts at LOOK 1 or  $> \pm 5$  counts at LOOK 2, the EPROM is reprogrammed to bring the pointing direction to within the prescribe tolerances. A copy of the STE computer print out showing the pointing direction is shown in Figure 2.

BP	Command	Actual		Difference*	
		Look 1	Look2	Look 1	Look2
1	6657	6655	6655	2	2
2	6505	6507	6503	-2	2
3	6353	6355	6350	-2	3
4	6202	6204	6199	-2	3
5	6050	6052	6049	-2	1
6	5898	5900	5897	-2	1
7	5747	5748	5746	-1	1
8	5595	5597	5593	-2	2
9	5443	5444	5440	-1	3
10	5292	5293	5288	-1	4
11	5140	5141	5138	-1	2
12	4988	4990	4987	-2	1
13	4837	4839	4834	-2	3
14	4685	4687	4684	-2	1
15	4533	4536	4532	-3	1
16	4382	4384	4381	-2	1

BP	Command	Actual		Difference*	
		Look 1	Look2	Look 1	Look2
17	4230	4232	4227	-2	3
18	4078	4080	4076	-2	2
19	3927	3928	3923	-1	4
20	3775	3777	3774	-2	1
21	3623	3627	3621	-4	2
22	3472	3475	3470	-3	2
23	3320	3323	3319	-3	1
24	3168	3171	3167	-3	1
25	3017	3020	3015	-3	2
26	2865	2868	2862	-3	3
27	2713	2716	2710	-3	3
28	2562	2564	2558	-2	4
29	2410	2412	2408	-2	2
30	2258	2260	2255	-2	3
CC 1	665	665	666	0	-1
WC	12650	12650	12651	0	-1

Figure 2. Beam Position Pointing Directions and Error Calculation

#### 5.5.4 GAIN/PHASE MARGIN

A gain/phase margin test was performed on the antenna drive subsystem. The test was performed by obtaining a Bode plot of the control loop and measuring the gain at 180° phase differential and the phase margin at the 0db crossover point.

The Dynamic Signal Analyzer (DSA) was used to make the measurement operating in the swept sine mode. Three separate Bode plots were made on the antenna and the gain and phase margins were determined from each plot. The gain margin measured was 13.5 db (average of three) and the phase margin measured was 60.7 degrees (average of three). These margins exceed the specification requirements of 12 db and 25 degrees and therefore are acceptable. The three Bode waveforms were plotted and are presented in Appendix D1 thru D3. The waveforms are also stored on the AMSU-A2 Test Data File disc. The test data sheet is presented in Appendix D4.

### 5.5.5 OPERATIONAL GAIN MARGIN

An operational gain margin test was performed on the instrument three times. This test consists of increasing the gain of the control loop until oscillation occurs. The gain increase and frequency of oscillation are measured. An increase in gain greater than 9 db is required; the frequency of oscillation is an observation.

A 50K pot was connected in series with the R58 feedback resistor on amplifier AR8. The resistance of the test pot was slowly added to the feedback resistor while observing the reflector for oscillations.

The reflector begins to produce an audible sound as gain is increased. The following added resistance values are calculated to have the following gain margins:

Resistance	Gain
38.58	9.3 db
41.20	9.7 db
42.88	9.9 db

The first mode mechanical resonance of the shaft and reflector is about 228 Hz as shown in the power spectrum. The power spectrum waveform was plotted and is presented in Appendix E1. The waveform is also stored on the AMSU-A2 Test Data File disc. The test data sheet is presented in Appendix E2.

## 6.0 CONCLUSION

Based on the test results, it can be concluded that the METSAT AMSU-A2 S/N 105 antenna drive subsystem meets the AMSU-A specification requirements.

## 7.0 TEST DATA

Test data for the AMSU-A2 S/N 105 obtained in the antenna drive subsystem test is attached. Data sheet number and type of test is shown in the following Appendix Index.

**APPENDIX INDEX**

<i>Appendix A1 .....</i>	<i>Resolver Data Isolator CCA TDS</i>
<i>Appendix A2 .....</i>	<i>Interface Converter CCA TDS</i>
<i>Appendix A3 .....</i>	<i>Motor Driver CCA TDS</i>
<i>Appendix A4 .....</i>	<i>R/D Converter/ Oscillator CCA TDS</i>
<i>Appendix B1 .....</i>	<i>Full Scan Step Response</i>
<i>Appendix B2 thru B30.....</i>	<i>Single Step Responses</i>
<i>Appendix B31 and B32.....</i>	<i>Cold Calibration Step Response</i>
<i>Appendix B33 and B34.....</i>	<i>Warm Calibration Step Response</i>
<i>Appendix B35 .....</i>	<i>Scan Motion Jitter Test TDS</i>
<i>Appendix C1 .....</i>	<i>Peak Pulse Load Bus Current Waveform</i>
<i>Appendix C2.....</i>	<i>Pulse Load Bus Current TDS</i>
<i>Appendix D1 thru D3 .....</i>	<i>Gain/ Phase Margin Bode Plots</i>
<i>Appendix D4.....</i>	<i>Gain/ Phase Margin TDS</i>
<i>Appendix E1 .....</i>	<i>Operational Gain Margin Power Spectrum</i>
<i>Appendix E2 .....</i>	<i>Operational Gain Margin TDS</i>

TEST DATA SHEET B-6 (Sheet 1 of 2)

RESOLVER DATA ISOLATOR CCA (P/N 1334972) (Paragraph 6.6.7)

Date: 4/14/97  
S/N: F-28  
1334972-1

6.6.7.1 Supply Voltages

Supply*	Measured Value (V)	Limits (Vdc)	Pass/Fail
+5 V (I)	5.00	$\pm 0.25$	P
+5 V (U)	5.01	$\pm 0.25$	P

6.6.7.2 Supply Currents

Steps 1 and 2:

Supply*	Measured Value (mA)	Limits (mA)	Pass/Fail
+5 V (I)	53.27	100 max	P
+5 V (U)	320.41	400 max	P

Steps 3 and 4:

Supply*	Measured Value (mA)	Limits (mA)	Pass/Fail
+5 V (I)	83.24	150 max	P
+5 V (U)	11.06	30 max	P

\* I = Isolated, U = Unisolated

6.6.7.3 Resolver Data

Bit No.	Pass/Fail
API 0 - AP Bit 0	P
API 1 - AP Bit 1	P
API 2 - AP Bit 2	P
API 3 - AP Bit 3	P
API 4 - AP Bit 4	P
API 5 - AP Bit 5	P
API 6 - AP Bit 6	P
API 7 - AP Bit 7	P
API 8 - AP Bit 8	P
API 9 - AP Bit 9	P
API 10 - AP Bit 10	P
API 11 - AP Bit 11	P
API 12 - AP Bit 12	P
API 13 - AP Bit 13	P

6.6.7.4 Converter Busy Pulse

Converter Busy Pulse	Measured Value ( $\mu$ sec)	Limits ( $\mu$ sec)	Pass/Fail
15.0	14.9	$\pm 3.0$	P

F28

TEST DATA SHEET B-6 (Sheet 2 of 2)

RESOLVER DATA ISOLATOR CCA (P/N 1334972) (Paragraph 6.6.7)

Comments:

NONE

Conducted by:

*Dennis Lee*

Test Engineer

*4/14/97*

Date

Verified by:

*Judith Henry*

Quality Control Inspector

*4-14-97*

Date

Approved by:

*[Signature]*

DCMC

*4/16/97*

Date



TEST DATA SHEET B-13 (Sheet 1 of 3)

INTERFACE/CONVERTER CCA (P/N 1331697) (Paragraph 6.13.7)

Date: 5/7/97  
CCA S/N: F23  
1331697-1

6.13.7.1 Supply Voltages

Supply	Measured Value (Vdc)	Limits (Vdc)	Pass/Fail
+5V (U)	+5.01V	+5V±0.05	P
+15V (I)	+15.03V	+15V±0.15	P
-15V (I)	-15.01V	-15V±0.15	P
+5V (I)	+5.00V	+5V±0.05	P

6.13.7.2 Supply Currents

Step 1 (CP and API Low):

Supply	Measured Value (mA)	Limits (mA)	Pass/Fail
+5V (U)	85.96 mA	70 - 110	P
+5V (I)	3.42 mA	1.5 - 5.5	P
+15V (I)	18.05 mA	15 - 23	P
-15V (I)	20.84 mA	18 - 26	P

Step 2 (CP and API High):

Supply	Measured Value (mA)	Limits (mA)	Pass/Fail
+5V (U)	56.24 mA	40 - 70	P
+5V (I)	23.84 mA	18 - 30	P
+15V (I)	18.05 mA	15 - 23	P
-15V (I)	20.84 mA	18 - 26	P

6.13.7.3 Amplifier Offsets

Amplifier	Measured Value (mV)	Limits (mV)	Pass/Fail
AR1	-0.01 mV	0.0±0.15	P
AR2	-0.26 mV	0.0±2.0	P

TEST DATA SHEET B-13 (Sheet 2 of 3)

INTERFACE/CONVERTER CCA (P/N 1331697) (Paragraph 6.13.7)

6.13.7.4 Subtraction and D-A Conversion

Step 1:

Actual Position (API) MSB      LSB	Command Position (CP) MSB      LSB	ARI Output* Voltage Required (Vdc)	Test Result (Vdc)	Pass/Fail
00000000000000	00000000000000	0.00000	-0.00001	P
000000000000001	00000000000000	-0.00061	-0.00067	P
000000000000010	00000000000000	-0.00122	-0.00134	P
000000000000011	00000000000000	-0.00184	-0.00197	P
000000000000100	00000000000000	-0.00245	-0.00261	P
0000000000001000	00000000000000	-0.00490	-0.00515	P
00000000000010000	00000000000000	-0.00979	-0.01025	P
000000000000100000	00000000000000	-0.01958	-0.02044	P
0000000000001000000	00000000000000	-0.03917	-0.04083	P
00000000000010000000	00000000000000	-0.07834	-0.08162	P
000000000000100000000	00000000000000	-0.15667	-0.16320	P
0000000000001000000000	00000000000000	-0.31334	-0.32639	P
00000000000010000000000	00000000000000	-0.62669	-0.65285	P
000000000000100000000000	00000000000000	-1.25338	-1.3059	P
0000000000001000000000000	00000000000000	-2.50675	-2.6117	P
00000000000010000000000000	00000000000000	-5.01350	-5.2235	P

\* Tolerance on output voltage is  $\pm 10\%$

Step 2:

Actual Position (API) MSB      LSB	Command Position (CP) MSB      LSB	ARI Output* Voltage Required (Vdc)	Test Result (Vdc)	Pass/Fail
00000000000000	00000000000000	0.00000	-0.00002	P
000000000000000	000000000000001	0.00061	+0.00055	P
0000000000000000	0000000000000010	0.00122	+0.00117	P
00000000000000000	0000000000000011	0.00184	+0.00174	P
000000000000000000	00000000000000100	0.00245	+0.00245	P
0000000000000000000	000000000000001000	0.00490	+0.00501	P
00000000000000000000	0000000000000010000	0.00979	+0.01014	P
000000000000000000000	00000000000000100000	0.01958	+0.020345	P
0000000000000000000000	000000000000001000000	0.03917	+0.04074	P
00000000000000000000000	0000000000000010000000	0.07834	+0.08154	P
000000000000000000000000	00000000000000100000000	0.15667	+0.16321	P
0000000000000000000000000	000000000000001000000000	0.31334	+0.32645	P
00000000000000000000000000	0000000000000010000000000	0.62669	+0.65306	P
000000000000000000000000000	00000000000000100000000000	1.25338	+1.3057	P
0000000000000000000000000000	000000000000001000000000000	2.50675	+2.6114	P
00000000000000000000000000000	0000000000000010000000000000	-5.01350	-5.2235	P

\* Tolerance on output voltage is  $\pm 10\%$

TEST DATA SHEET B-13 (Sheet 3 of 3)

INTERFACE/CONVERTER CCA (P/N 1331697) (Paragraph 6.13.7)

6.13.7.5 Strobe Function

Step 1: Strobe Low

No E11 Change  
with Input CP Changes

Pass/Fail

P

Step 2: Strobe High

E11 Change  
with Input CP Changes

Pass/Fail

P

6.13.7.6 Amplifier Gain

	<u>Measured Value (Vdc)</u>	<u>Limits (Vdc)</u>	<u>Pass/Fail</u>
E11	<u>0.32645</u>	-	<u>P</u>
E10	<u>3.5913</u>	-	<u>P</u>
<u>E10 Voltage</u> <u>E11 Voltage</u>	<u>11.0</u>	10.7 - 11.3	<u>P</u>

6.13.7.7 Ground Isolation

	<u>Measured Value (MΩ)</u>	<u>Limits (MΩ)</u>	<u>Pass/Fail</u>
Pin 91 to Pin 7 DC Resistance	<u>200MΩ</u>	>20	<u>P</u>

Comments:

NONE

Conducted by:

Dennis Lum  
Test Engineer

5/1/97  
Date

Verified by:

Judith Hervey  
Quality Control Inspector

5/8/97  
Date

Approved by:

BCM

5/10/97  
Date

TEST DATA SHEET B-4 (Sheet 1 of 2)

MOTOR DRIVER 3-HALL SENSOR CCA (P/N 1331694) (Paragraph 6.4.3)

S/N: F06  
Date: 4/21/97  
1331694-4  
6.4.3.2 Input Signal Offset

Step No.	Test Results	Limits
4	1.78 mV	0.0 ± 1 mVdc
6	1.40 mV	0.0 ± 1 mVdc
8	1.60 mV	0.0 ± 1 mVdc

Step No.	Test Resistor	Resistance Measured
13	E7-E8 (R25)	3.4k
	E9-E10 (R52)	6.76k
	E11-E12 (R33)	3.16k
	E13-E14 (R53)	5.20k
	E15-E16 (R42)	3.16k
	E17-E18 (R54)	3.61k

Step No.	Resistors	Selected Trim Resistors
14	R25	RNC55J3401FS
	R52	RNC55J6311FS
	R33	RNC55J3161FS
	R53	RNC55J5231FS
	R42	RNC55J3161FS
	R54	RNC55J5621FS

Step No.	E Point	Test Results	Limits	Pass/Fail
19	E19	-0.07 mV	0.0 ± 1 mVdc	P
	E20	-0.11 mV	0.0 ± 1 mVdc	P
	E21	-0.12 mV	0.0 ± 1 mVdc	P

6.4.3.3 Motor Driver Operation

Clockwise Rotation:

Step No.	Test Results	Limits	Pass/Fail
2	4.99V	+5V ± 0.05Vdc	P
	58.6 mA	70mA dc max	P
	15.07V	+15V ± 0.15Vdc	P
	1.5 mA	3.0mA dc max	P
	-14.98V	-15V ± 0.15Vdc	P
	18.8 mA	25mA dc max	P
	28.01V	+28V ± 0.5Vdc	P
	5.6 mA	8mA dc max	P
3	344 mV	400mVdc max	P
4	44.5 mA	50mA dc max	P
5	47.2 mA	50mA dc max	P

26693A  
J Feb 97

TEST DATA SHEET B-4 (Sheet 2 of 2)

MOTOR DRIVER 3-HALL SENSOR CCA (P/N 1331694) (Paragraph 6.4.3)

Counter Clockwise Rotation:

Step No.	Test Results	Limits	Pass/Fail
3	282 mV	400mVdc max	P
4	36.6 mA	50mAdc max	P
5	39.9 mA	50mAdc max	P

6.4.3.4 Current Limit Test

Step No.	Test Results	Limits	Pass/Fail
2	439 mA	350-500mAdc	P

Comments: NONE

Conducted by:

Dennis Lee  
Test Engineer

4/21/97  
Date

Verified by:

Judith Horne  
Quality Control Inspector

04/28/97  
Date

Approved by:

[Signature]  
DCMC

4/29/97  
Date

TEST DATA SHEET B-4 (Sheet 1 of 2)

MOTOR DRIVER 3-HALL SENSOR CCA (P/N 1331694) (Paragraph 6.4.3)

S/N: F09  
Date: 4/21/97  
1331694-4  
6.4.3.2 Input Signal Offset

Step No.	Test Results	Limits
4	1.53 mV	0.0 ± 1 mVdc
6	1.11 mV	0.0 ± 1 mVdc
8	1.21 mV	0.0 ± 1 mVdc

Step No.	Test Resistor	Resistance Measured
13	E7-E8 (R25)	3.40k
	E9-E10 (R52)	5.97k
	E11-E12 (R33)	3.16k
	E13-E14 (R53)	4.55k
	E15-E16 (R42)	3.40k
	E17-E18 (R54)	5.23k

Step No.	Resistors	Selected Trim Resistors
14	R25	RNC55J3401FS
	R52	RNC55J6041FS
	R33	RNC55J3161FS
	R53	RNC55J4521FS
	R42	RNC55J3401FS
	R54	RNC55J5231FS

Step No.	E Point	Test Results	Limits	Pass/Fail
19	E19	0.02 mV	0.0 ± 1 mVdc	P
	E20	0.08 mV	0.0 ± 1 mVdc	P
	E21	0.10 mV	0.0 ± 1 mVdc	P

6.4.3.3 Motor Driver Operation

Clockwise Rotation:

Step No.	Test Results	Limits	Pass/Fail
2	5.00V	+5V ± 0.05Vdc	P
	51.9 mA	70mA max	P
	15.07V	+15V ± 0.15Vdc	P
	1.5 mA	3.0mA max	P
	-14.98V	-15V ± 0.15Vdc	P
	18.6 mA	25mA max	P
	28.03V	+28V ± 0.5Vdc	P
	5.6 mA 0.2uF	8mA max	P
3	279 mV	400mVdc max	P
4	42.6 mA	50mA max	P
5	47.9 mA	50mA max	P

AE-26693A  
10 Feb 97

TEST DATA SHEET B-4 (Sheet 2 of 2)

MOTOR DRIVER 3-HALL SENSOR CCA (P/N 1331694) (Paragraph 6.4.3)

Counter Clockwise Rotation:

Step No.	Test Results	Limits	Pass/Fail
3	271 mV	400mVdc max	P
4	36.4 mA	50mAdc max	P
5	39.7 mA	50mAdc max	P

6.4.3.4 Current Limit Test

Step No.	Test Results	Limits	Pass/Fail
2	440 mA	350-500mAdc	P

Comments:

NONE

Conducted by:

Test Engineer

Date

Verified by:

Quality Control Inspector

Date

Approved by:

DCMC

Date

## TEST DATA SHEET B-5 (Sheet 1 of 3)

## R-D CONVERTER/OSCILLATOR CCA (P/N 1337739) (Paragraph 6.5.7)

Date 8/26/97  
 CCA S/N F09  
1337739-2

6.5.7.1 UUT Pre-Test

Step 2:

## Supply Currents (Without UUT)

Supply (Vdc)	(Baseline) Measured Value (mA) (Without UUT)	Limits (mA)	Pass/Fail
+15	0.06	0-1	P
-15	-0.29	-1-0	P
+5	0.06	0-1	P

## Supply Voltages (Without UUT)

Supply	Measured Value (V)	Limits (V)	Pass/Fail
+15V (I)	15.02	± 0.50	P
-15V (I)	-15.01	± 0.50	P
+5V (I)	5.03	±0.25	P

Step 6:

## Supply Currents (UUT Installed)

Supply (Vdc)	Measured Value (mA) (UUT Installed)	Difference (mA) (Measured - Baseline)	Limits (mA)	Pass/Fail
+15	32.82	32.76	20-40	P
-15	-41.07	-40.78	-30 - -50	P
+5	55.05	54.99	30-70	P

6.5.7.2 Supply Voltages (UUT Installed)

Supply	Measured Value (V)	Limits (V)	Pass/Fail
+15V (I)	15.01	± 0.50	P
-15V (I)	-14.96	± 0.50	P
+5V (I)	5.02	±0.25	P

6.5.7.3 Oscillator Frequency, Duty Cycle, and Output Voltage

Parameter	Measured Value	Limits	Pass/Fail
Frequency	1600 Hz	1550-1650 Hz	P
Duty Cycle	51%	45-55 %	P
Output Voltage	8.19V	7.6-8.4 Vrms	P



TEST DATA SHEET B-5 (Sheet 2 of 3)

R-D CONVERTER/OSCILLATOR CCA (P/N 1337739) (Paragraph 6.5.7)

6.5.7.4 R-D Converter Operation

Step 1:

Bit Number/ Test Fixture Label	CW Pass/Fail	CCW Pass/Fail
API 0/1	P	P
API 1/2	P	P
API 2/3	P	P
API 3/4	P	P
API 4/5	P	P
API 5/6	P	P
API 6/7	P	P
API 7/8	P	P
API 8/9	P	P
API 9/10	P	P
API 10/11	P	P
API 11/12	P	P
API 12/13	P	P
API 13/14	P	P
Converter Busy	P	P

Step 2:

RS (E10)	Measured Value (Vdc)	Calculated Value (Vdc) * CCA -1 Assy	Calculated Value (Vdc) * CCA -2 Assy	Pass/Fail
CW Rotation**	1.472	(+) N/A	(+) 1.790	P
CCW Rotation**	-1.635	(-) N/A	(-) 1.790	P

\* Signal level function of test and calibration gain resistors. Record calculated value and measured value. Measured value shall be within  $\pm 10$  percent of calculated value. The equation is as follows:

$$V = \pm 0.155 \left( \frac{R_{20}}{R_{17}} \right) \pm 10\%$$

$$= 0.155 \left( \frac{59k}{5.11k} \right) = 1.790V$$

20  
 223  
 8-26-97  
 20  
 223  
 8-26-97

6.5.7.5 Amplifier Gain

PES-RS	Measured Value (Vdc)	Limits (Vdc)	Pass/Fail
PES = +0.300 Vdc	1.162 V	1.00 to 1.30	P
PES = -0.300 Vdc	1.086 V	1.00 to 1.30	P

6.5.7.6 Direction Control Signal

DIR CNTRL	Measured Value (Vdc)	Limits (Vdc)	Pass/Fail
CW Rotation	5.00 V	4.5 to 5.5	P
CCW Rotation	0.121 V	0.0 to 0.4	P

TEST DATA SHEET B-5 (Sheet 3 of 3)

R-D CONVERTER/OSCILLATOR CCA (P/N 1337739) (Paragraph 6.5.7)

6.5.7.7 Notch Filter Frequency Response

Frequency	Measured Value (Hz)	Calculated Value (Hz) * CCA -1 Assy	Calculated Value (Hz) * CCA -2 Assy	Pass/Fail
AR3 Notch	N/A	N/A	N/A	N/A
AR4 Notch	↓	↓	↓	↓
AR5 Notch	↓	↓	↓	↓

\* Notch frequencies shall be within  $\pm 3$  percent of values determined by test and calibration resistors. Record calculated and measured values.

Comments:

NONE

Conducted by:

Test Engineer

7A  
268

Date

8/26/97

NOV 19 '97

Verified by:

Quality Control Inspector

Date

Approved by:

DCMC

Date

11-19-97

CAP TIM BUF

36.0

4.5

/DIV

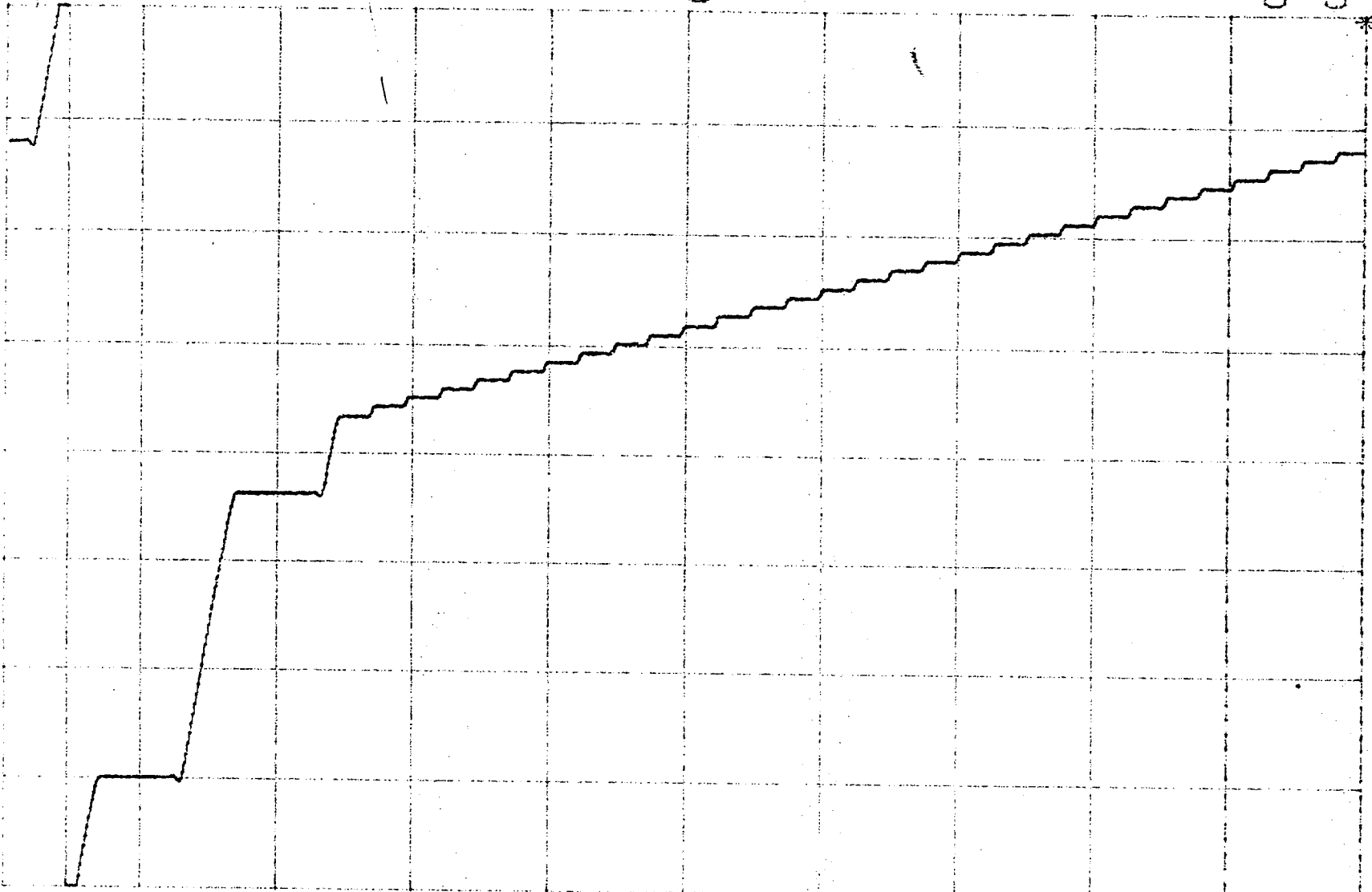
READ

V

0.0

FXPXY 0.0

SBO



HETSA7 ANSU-A2 105

AE26002/20 PMA: 3.4.5.4 step 74  
file: 7AP-F55  
268  
MAY 28 98

SCENE 2

FILE NAME: 7AP FS1

X=202.3mS

$\Delta X = 38.67\text{mS}$

Y=5.79806

$\Delta Y = 24.24\text{mV}$

Y<sub>a</sub>=5.811

$\Delta Y_a = 364.9\text{mV}$

CAP TIM BUF

6.2

100

m

/Div

Real

V

5.4

FxdXY 131m

Sec

408m

JITTER =  $\frac{3.6}{4.3\%}$

OVERSHOOT =  $\frac{1.6\%}{1.6\%}$

AMSU-A2  
S/N 105



MAY 28 '98

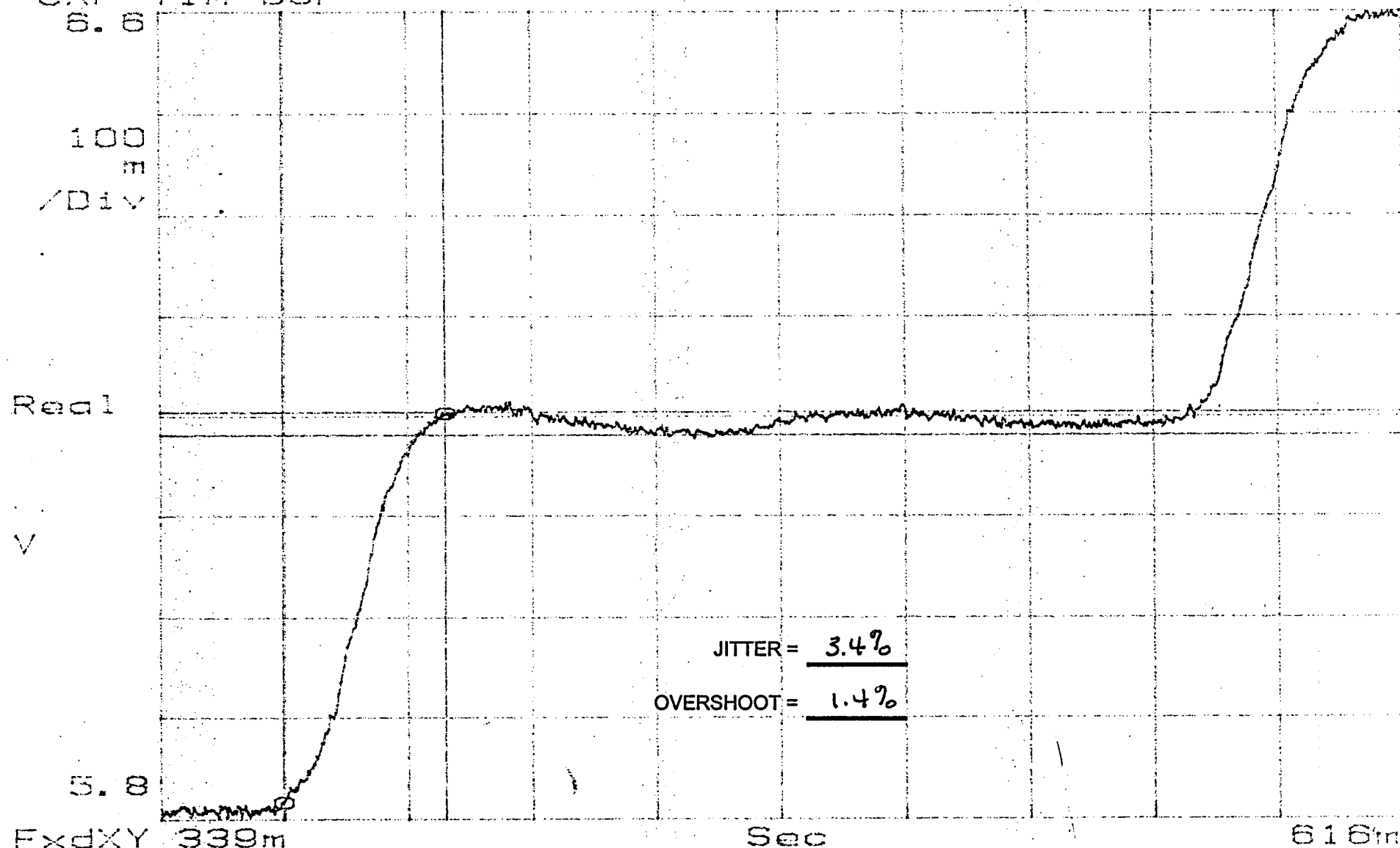
AE26002/2D

PARA: 3.4.5.5 Step 9

b2

X=366.8ms     $\Delta X=36.33\text{ms}$     Y=6.20388     $\Delta Y=23.76\text{mV}$   
Y<sub>a</sub>=5.81586     $\Delta Y_a=386.0\text{mV}$

CAP TIM BUF  
6.6



AMSU-A2  
S/N 105

7A  
268

MY 28 '98

AE26002/2D  
PARA: 3.4.5.5 Step 10

FILE NAME: 7AP FS1

SCEN 4

$\Delta Y = 22.79 \text{ mV}$

$Y = 6.56885$

$X = 604.7 \text{ ms}$   
 $\Delta X = 32.81 \text{ ms}$   
 $\Delta Y = 369.8 \text{ mV}$

CAP TIM BUF

6.98

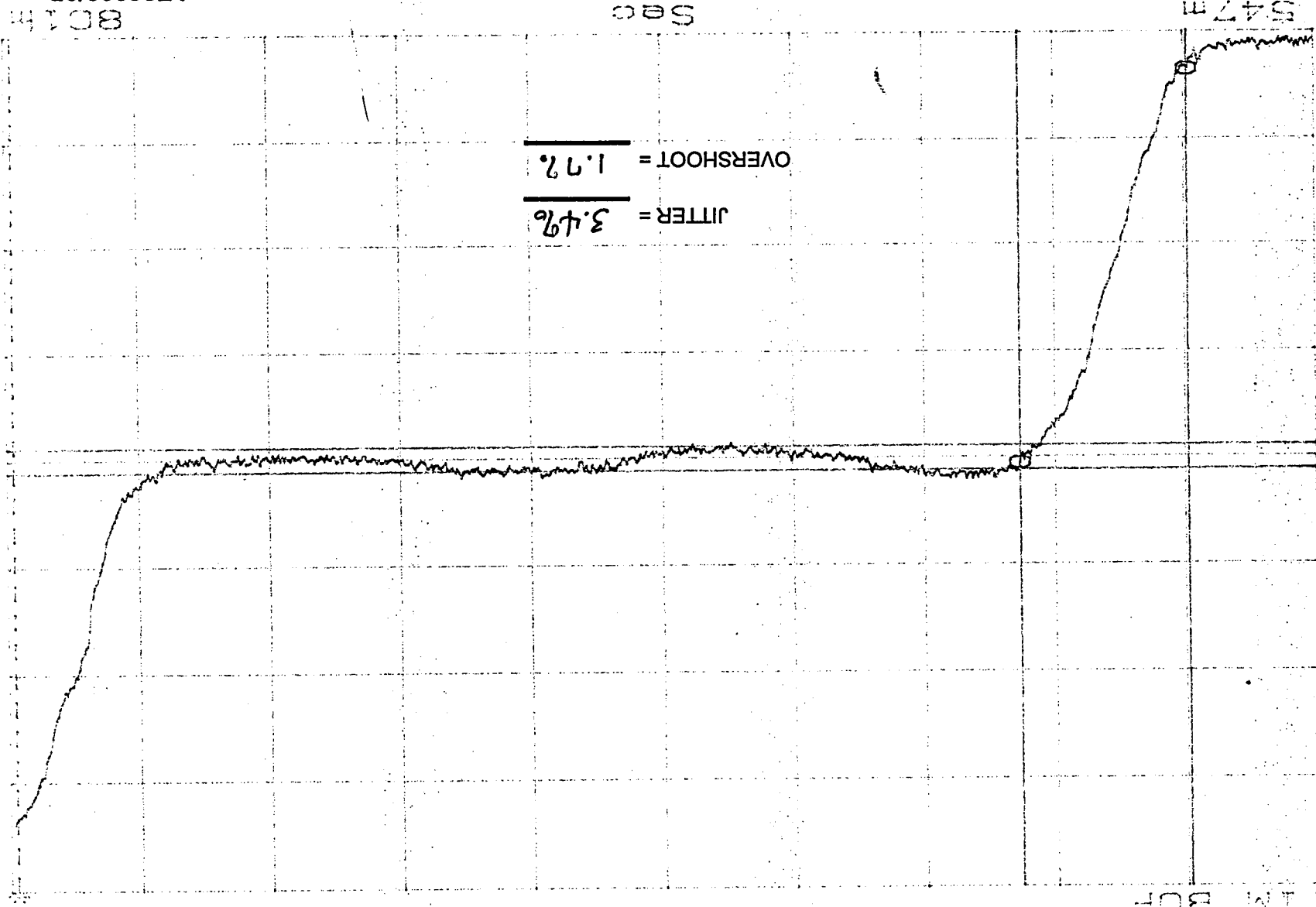
100

/DIV

Reg 1

6.18

FXY 547m



AE26002/2D  
PARA: 3.4.5 Step 11

AMSU-AZ  
S/N 105  
7A  
268  
MAY 98

SCEN 5  
FILE NAME: TAP FS1

$\Delta Y = 15.03 \text{ mV}$

$Y = 6.95$

$\Delta X = 38.28 \text{ ms}$   
 $\Delta Y = 364.9 \text{ mV}$

$X = 809.8 \text{ ms}$   
 $Y = 6.95925$

CAP TIM BUF \*

7.35

100

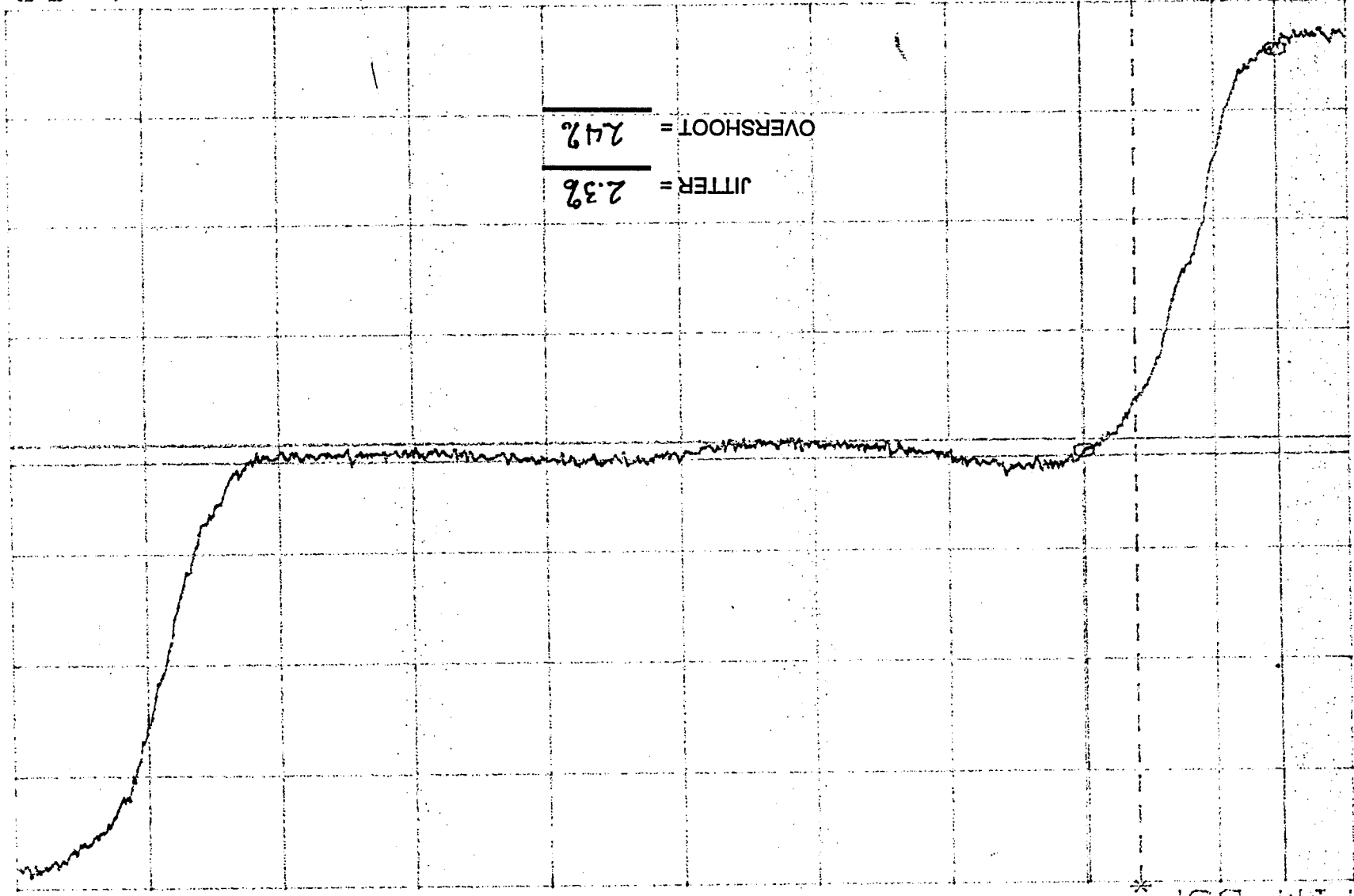
m  
/DIV

Rec 1

V

6.55

FXPXY 758m



JITTER = 2.3%  
OVERTSHOOT = 2.4%

Sec

1.02

AE26002/2D  
 PARA: 3.4.5.5 Step 12

AMSU-A2  
 S/N 105  
 7A  
 268  
 MAR 88 '98

35

X=1.009 S     $\Delta X=35.16\text{ms}$     Y=7.32873     $\Delta Y=10.67\text{mV}$   
Y<sub>a</sub>=7.31767     $\Delta Y_a=350.3\text{mV}$

CAP TIM BUF  
7.72

100  
m  
/Div

Real

V

JITTER = 1.6%  
OVERSHOOT = 1.6%

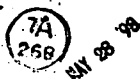
6.92

FxdXY 959m

Sec

1.20

AMSC-A2  
S/N 105



AE26002/2D  
PARA: 3.4.5.5 Step 13

26



SCEN 1  
FILE NAME: 7AP FS1

$X=1.214 \text{ S}$   
 $\Delta X=37.5 \text{ mS}$   
 $Y=7.72582$   
 $\Delta Y=20.85 \text{ mV}$

CAP TIM BUF

8. 12

001

44

^ T Q /

1002

A

7-10

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AMSU-A2  
S/N: 105

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268

May 88

025

OVERSHOOT = 7.8%

JITTER = 3.17%

AE26002/2D  
PARA: 3.4.5.5 Step 14

124

FILE NAME: TAP FS1

$\Delta Y = 22.3 \text{ mV}$

$Y = 8.09824$

$\Delta X = 35.94 \text{ ms}$   
 $\Delta Y = 361.7 \text{ mV}$

CAP TIM BUF

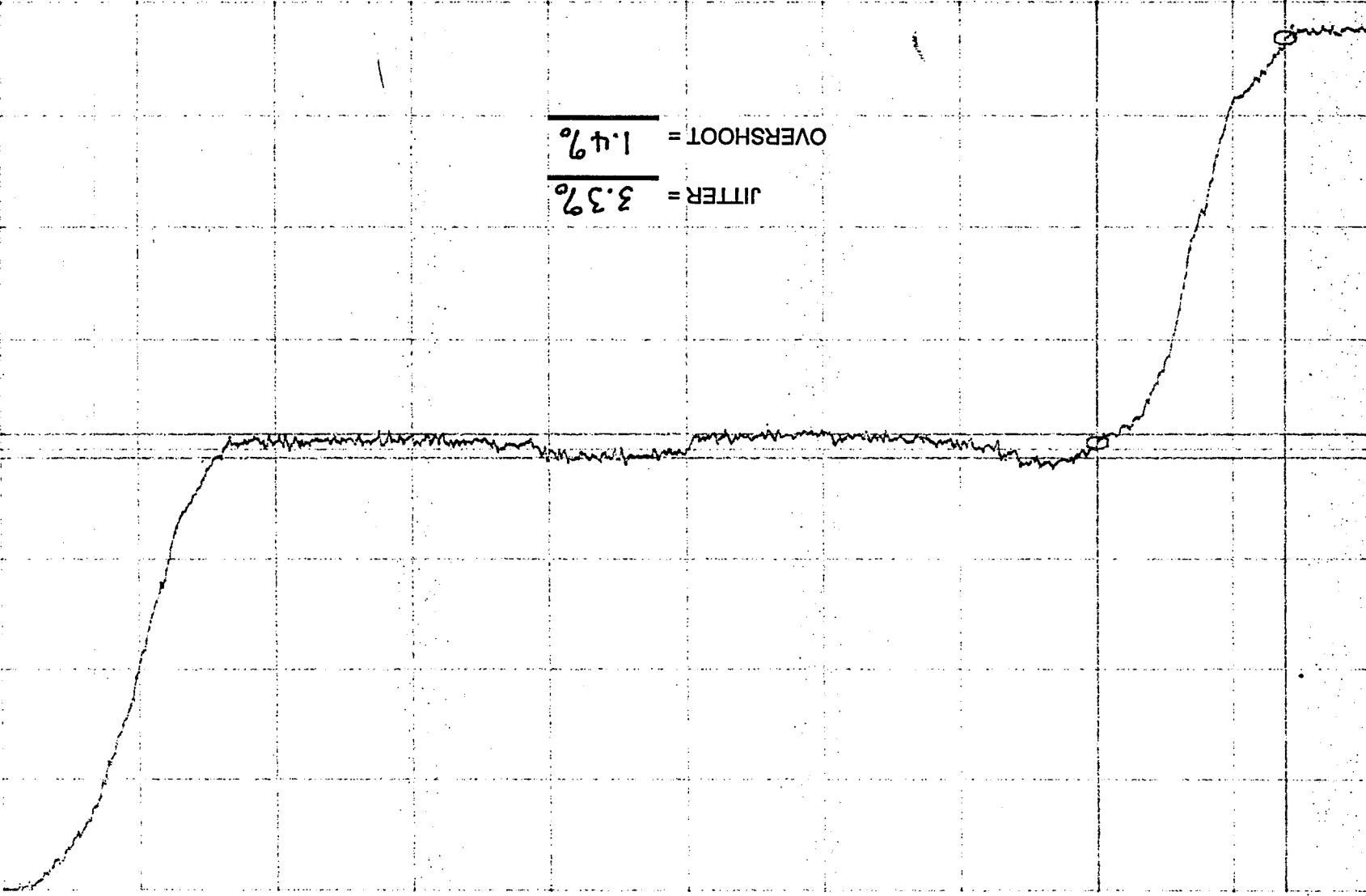
8.49

100

/DIV

Reg1

V



EXPXY 1.36

7.69

Sec

1.52

AE26002/2D  
PARA: 3.4.5 Step 15

AMSU-A2  
S/N 105  
268  
74  
MAR 88

88

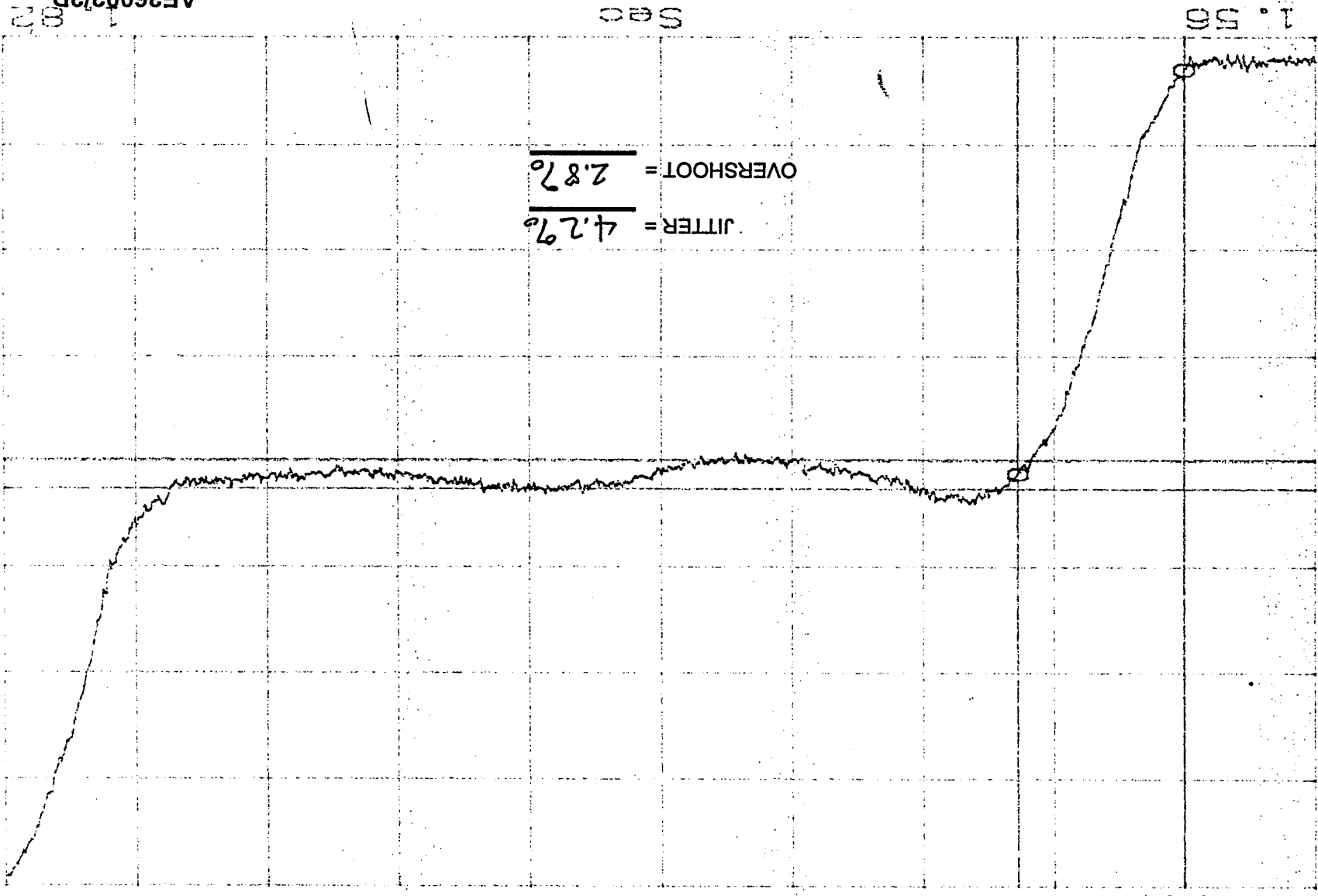
X=1.615 S ΔX=33.2ms Y=8.48618 ΔY=28.12mV  
Y0=8.47079 ΔY0=381.1mV

CAP TIM BUF 8.86

100  
/DIV  
m

Reg 1

V



Fxdxy 1.56

8.06

74  
268

AMSU-A2  
S/N 105

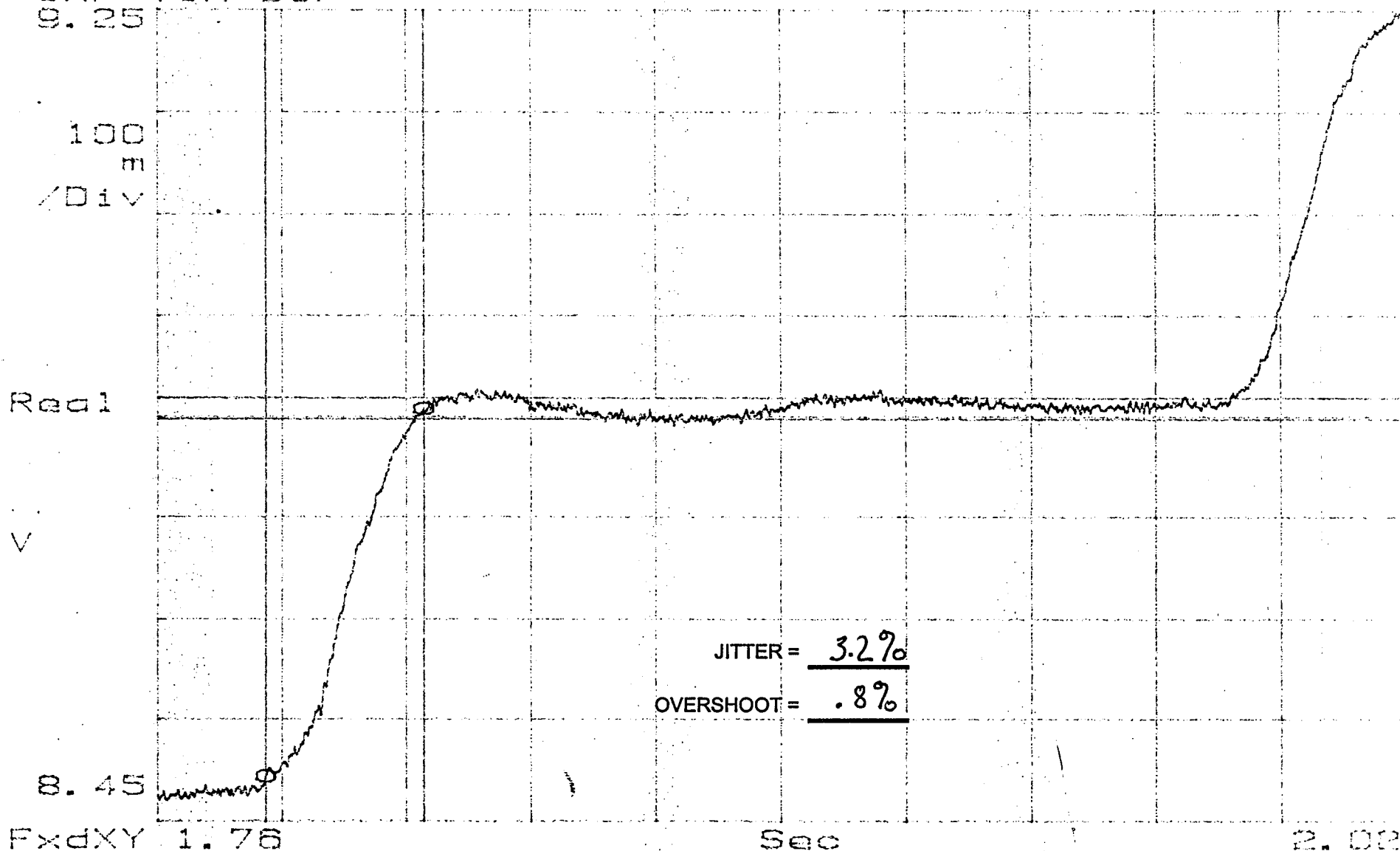
MAY 28 '98

PARA: 3.4.5.5 Step 16

AE26002/2D

X=1.818 S  $\Delta X=33.2\text{mS}$  Y=8.86843  $\Delta Y=21.33\text{mV}$   
Y<sub>a</sub>=8.85678  $\Delta Y_a=363.3\text{mV}$

CAP TIM BUF  
9.25



AMSu-A2  
S/N 105

7A  
260  
MAY 28 98

AE26002/2D  
PARA: 3.4.5.5 Step 17

SCEN 11  
FILE NAME: TAP FS1

$\Delta Y = 22.3 \text{ mV}$

$Y = 9.26327$

$X = 2.025 \text{ S}$   
 $\Delta X = 36.33 \text{ ms}$   
 $\Delta Y = 379.5 \text{ mV}$

CAP TIM BUF

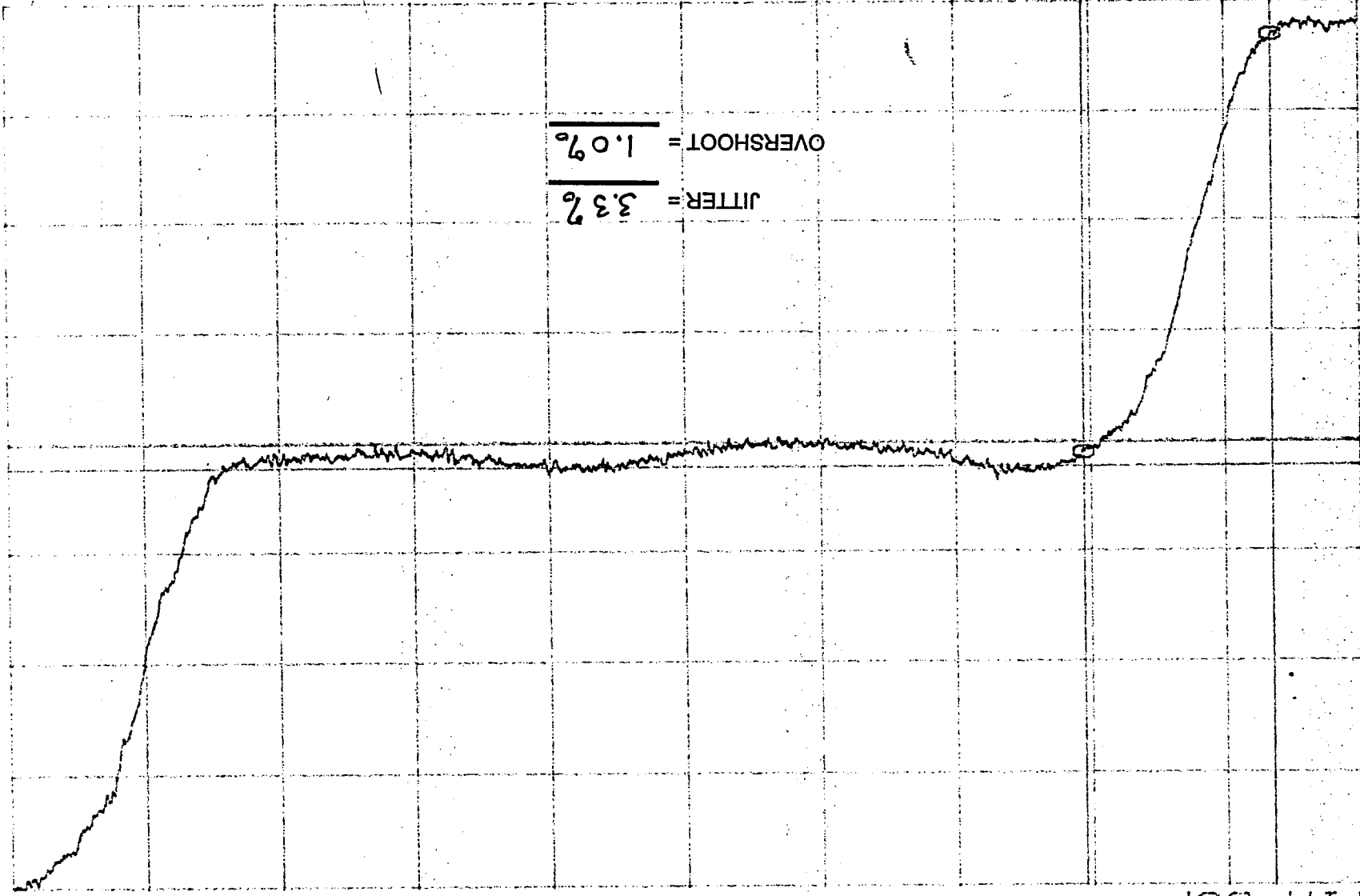
9.64

100

/DIV

Reol

V



Fxdxy 1.97

8.84

7A  
268

AMSU-A2  
S/N 105

11 28 98

AE26002/2D  
PARA: 3.4.5 Step 18

107

FILE NAME: 7AP FS1

X=2.225 S    ΔX=33.59ms    Y=9.63533    ΔY=19.39mV  
Y0=9.62877    ΔY0=353.6mV

CAP TIM BUF

10.0

100

/DIV

Reel

V

9.23

Fxdxy 2.17

Sec

OVERSHOOT= 1.8%

JITTER= 2.9%

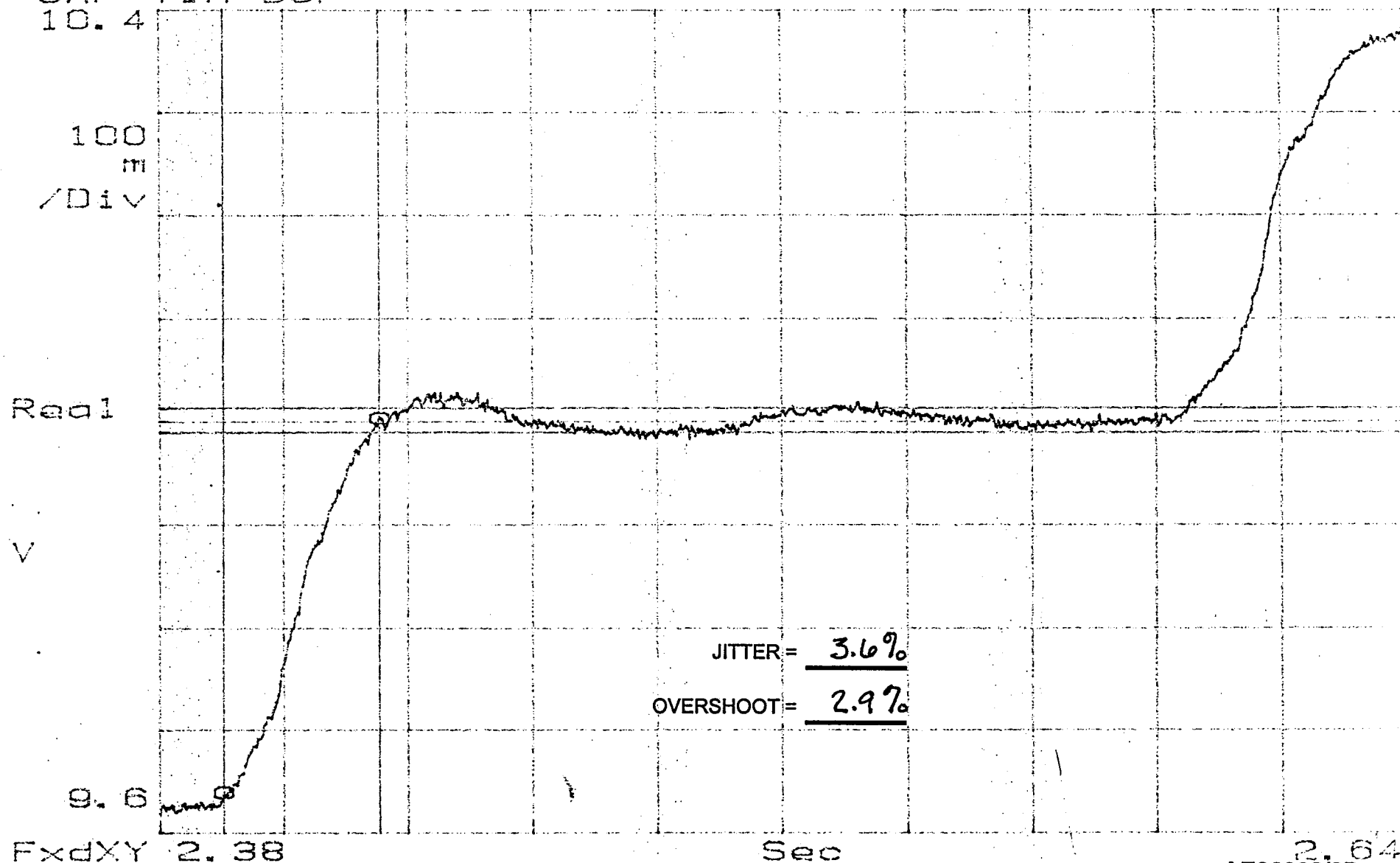
AE26002/2D  
PARA: 3.4.5.5 Step 19

7A  
268  
MAY 28 '98

AMSU-A2  
5/11 105

X=2.425 S     $\Delta X=32.81\text{ms}$     Y=10.0126     $\Delta Y=24.24\text{mV}$   
Y<sub>a</sub>=10.0018     $\Delta Y_a=363.3\text{mV}$

CAP TIM BUF  
10.4



AMSu-AZ  
S/N 105 (74)  
268  
MY 28 '98

AE26002/2D  
PARA: 3.4.5.5 Step 20

X=2.632 S     $\Delta X=37.89\text{mS}$     Y=10.3693     $\Delta Y=16.49\text{mV}$   
Y<sub>a</sub>=10.3683     $\Delta Y_a=360.0\text{mV}$

CAP TIM BUF  
10.8

100  
m  
/Div

Real

V

9.98

FxdXY 2.58

Sec

2.84

JITTER = 2.5%

OVERSHOOT = 2.5%

AMSU-AZ

S/N 105



JAY 28

AE26002/2D

PARA: 3.4.5.5 Step 21



SCEN 13  
FILE NAME: 7AP FS1

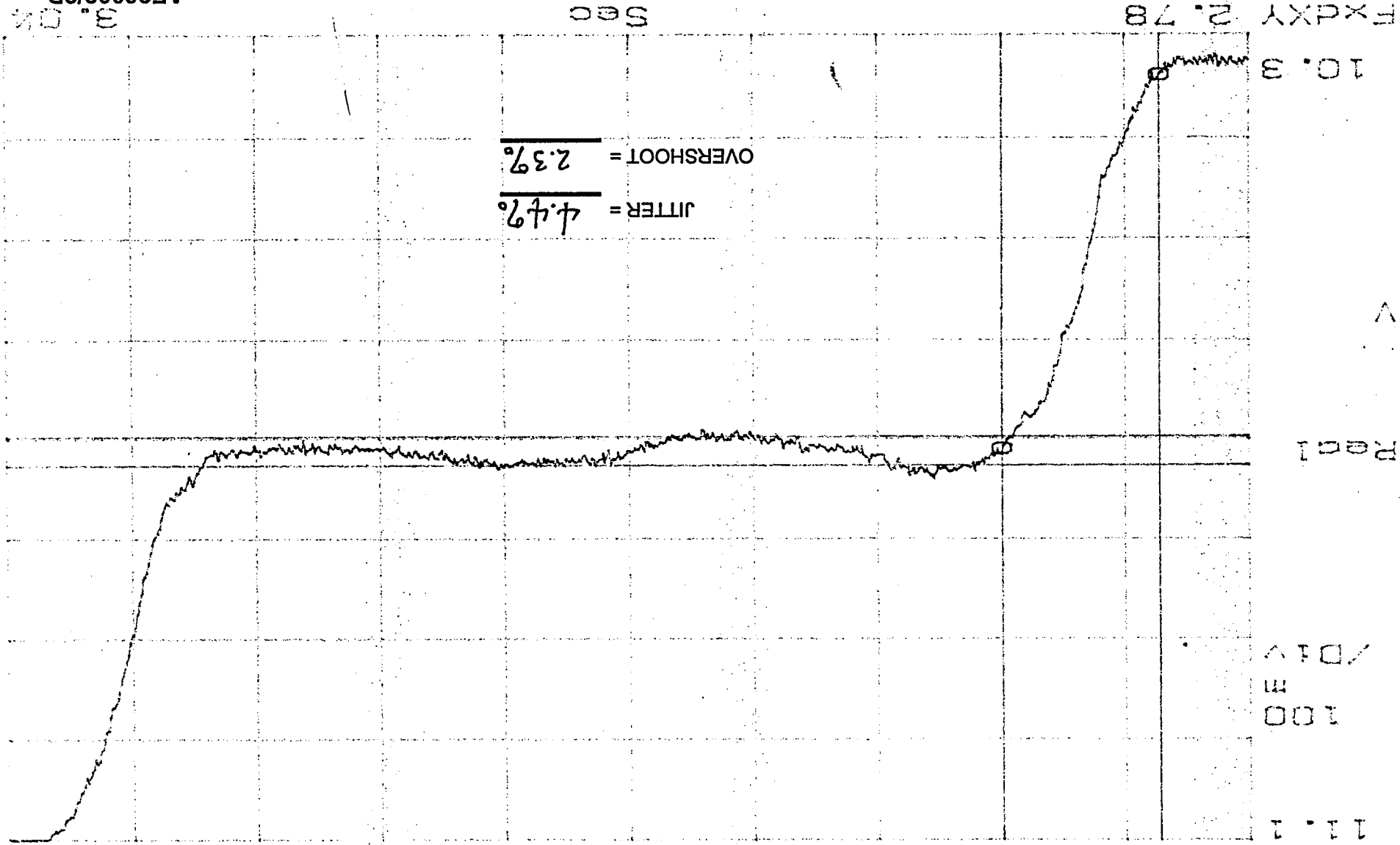
X=2.831 S  
Y=10.7478  
ΔX=33.2ms  
ΔY=369.8mV  
Y=10.7662  
ΔY=29.58mV

CAP TIM BUF  
11.1

100  
m  
/DIV

Reg1

V



3.0%  
AE26002/2D  
PARA: 3.4.5.5 Step 22

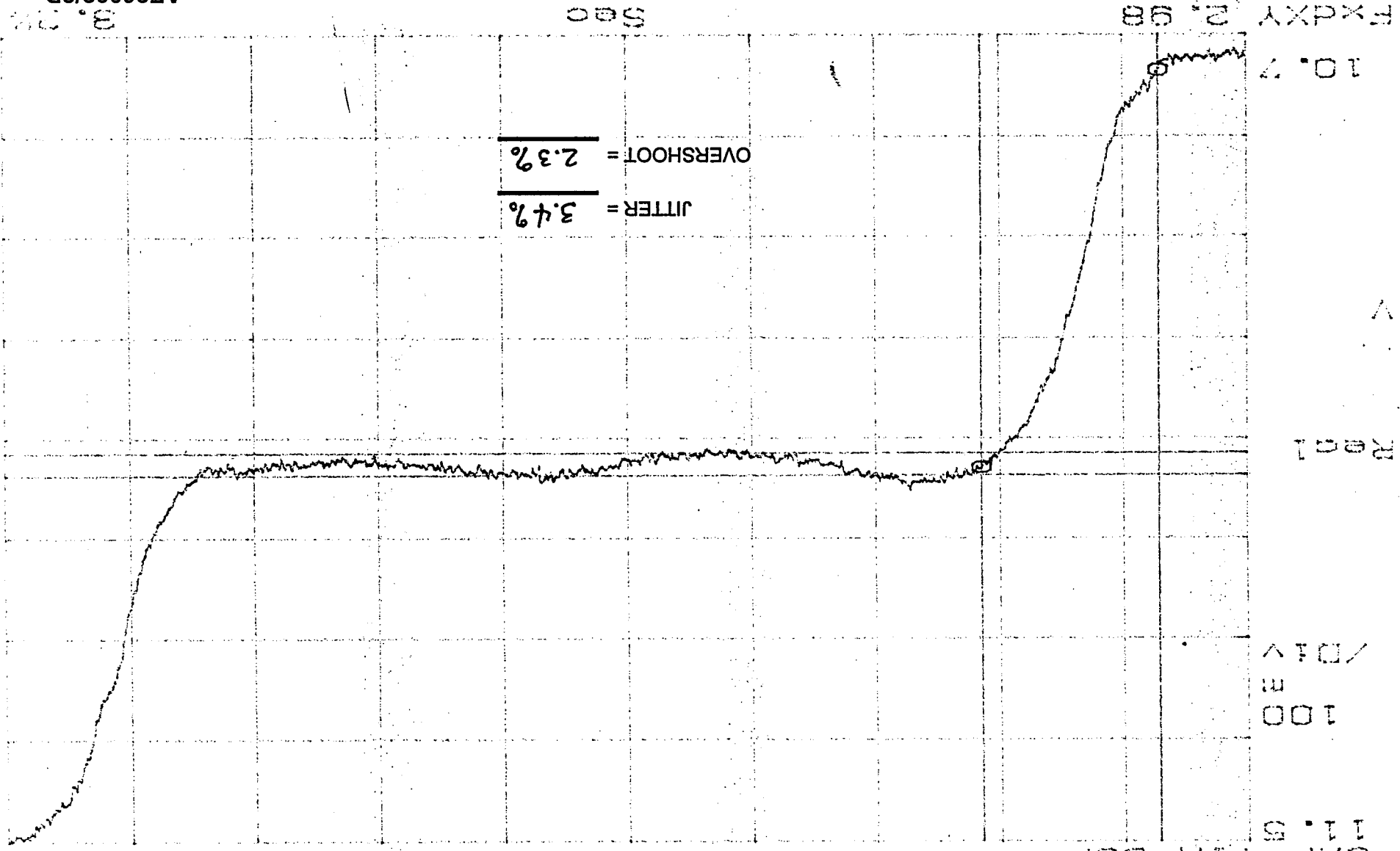
AMSU-A2  
S/N 105  
7A  
268

MAR 28 '98

FILE NAME: 7AP FS1

X=3.037 S ΔX=37.5ms Y=11.1668 ΔY=22.79mV  
Y0=11.1581 ΔY0=394.1mV

CAP TIM BUF 11.5



AE26002/2D  
PARA: 3.4.5.5 Step 23

AMSU-AZ  
8/1105

268  
7A  
MAY 28 '98

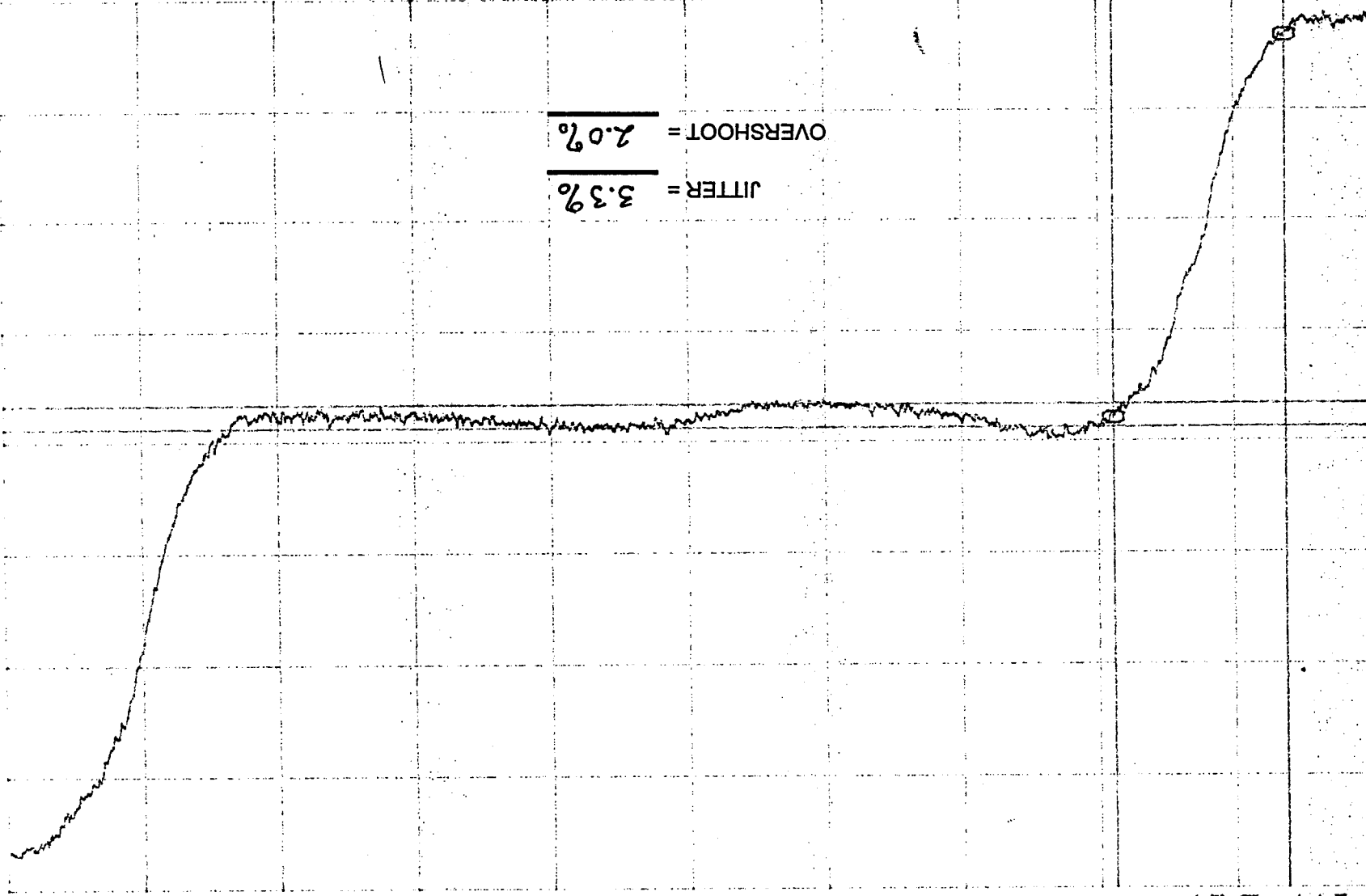
SCEN 11  
FILE NAME: ZAP FS1

X=3.236 S    ΔX=33.2ms    Y=11.5279    ΔY=21.82mV  
Y0=11.5182    ΔY0=345.4mV

CAP TIM BUF 11.8

100  
mV/DIV

Reg 1



JITTER = 3.3%  
OVERSHOOT = 2.0%

Sec

FXDXY 3.15

AE26002/2D  
PARA: 3.4.5.5 Step 24

AMSC-A2  
S/N 105  
7A  
268

X=3.438 S  $\Delta X=33.59\text{ms}$  Y=11.9005  $\Delta Y=32.0\text{mV}$   
Y<sub>a</sub>=11.8815  $\Delta Y_a=358.4\text{mV}$

CAP TIM BUF  
12.3

100  
m  
/Div

Real

V

JITTER = 4.8%

OVERSHOOT = 2.6%

11.5

FxdXY 3.39

Sec

AMSU-A2  
S/N 105

7A  
268  
REV 28 78

AE26002/2D  
PARA: 3.4.5.5 Step 25

B11

X=3.639 S     $\Delta X=32.42\text{mS}$     Y=12.2707     $\Delta Y=17.45\text{mV}$   
Y<sub>a</sub>=12.2626     $\Delta Y_a=368.2\text{mV}$

CAP TIM BUF  
12.7

100  
m  
/Div

Real

V

JITTER = 2.6%  
OVERSHOOT = 1.1%

11.9

FxdXY 3.59

Sec

AE26002/2D

PARA: 3.4.5.5 Step 26

AMSU-AZ  
S/N 105

7A  
268

MAY 28 '98

X=3.842 S     $\Delta X=31.25\text{ms}$     Y=12.6885     $\Delta Y=28.61\text{mV}$   
Ya=12.6664     $\Delta Ya=392.5\text{mV}$

CAP TIM BUF  
13.0

100  
m  
/Div

Real

V

JITTER = 4.3%  
OVERSHOOT = 2.3%

12.2

FxdXY 3.79

Sec

4.05

AMSd-AZ  
S/N 105



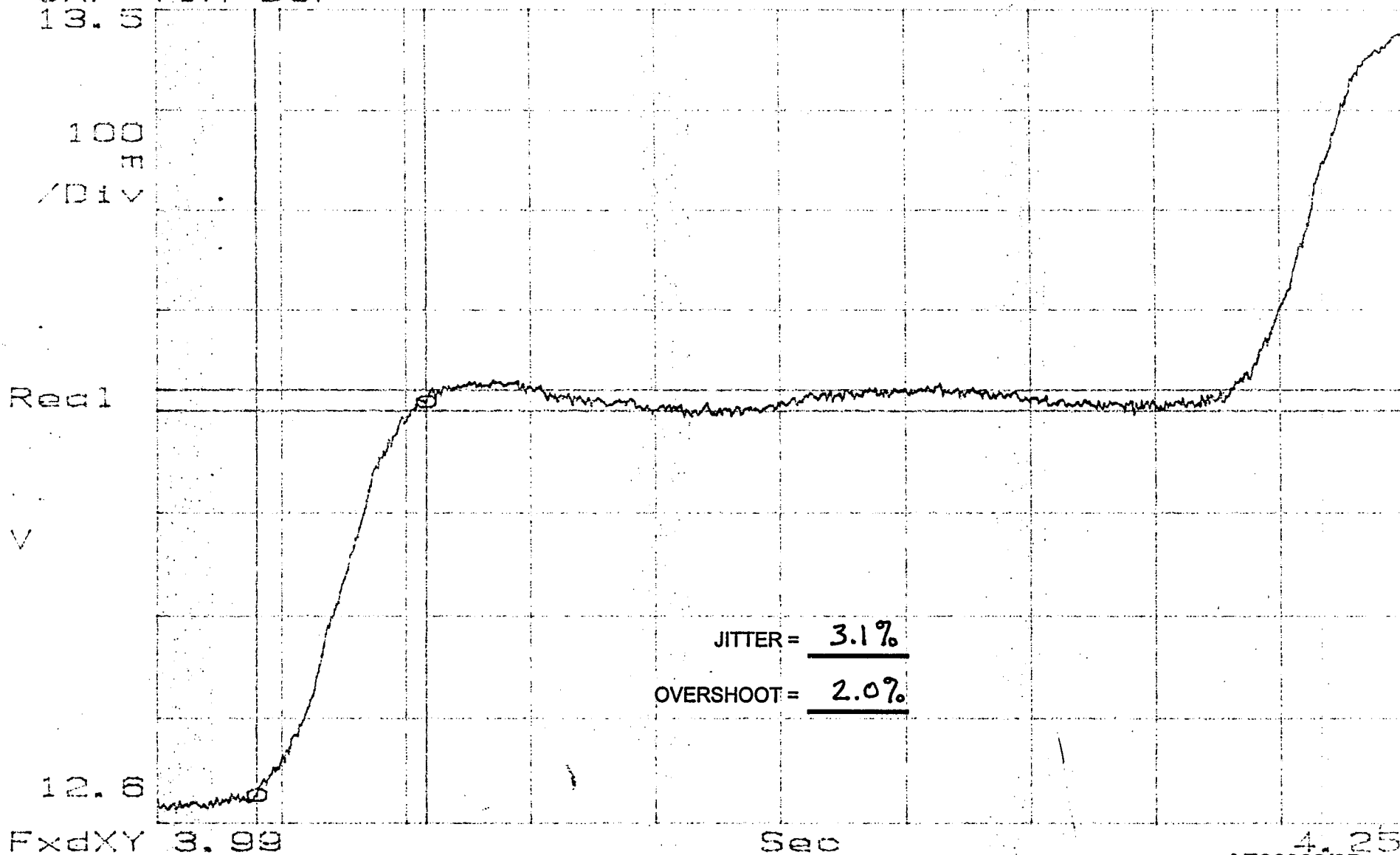
MAY 28 '98

AE26002/2D  
PARA: 3.4.5.5 Step 27

FOR

X=4.046 S     $\Delta X=35.55\text{ms}$     Y=13.0704     $\Delta Y=20.85\text{mV}$   
Y<sub>0</sub>=13.0573     $\Delta Y_0=381.1\text{mV}$

CAP TIM BUF  
13.5



AUSA-AZ  
S/N 105

7A  
268

MY 28 198

AE26002/2D  
PARA: 3.4.5.5 Step 28

X=4.247 S     $\Delta X=32.81\text{mS}$     Y=13.4348     $\Delta Y=23.27\text{mV}$   
Y<sub>a</sub>=13.4174     $\Delta Y_a=345.4\text{mV}$

CAP TIM BUF  
13.8

100  
m  
/Div

Real

V

13.0

FxdXY 4.19

Sec

4.45

JITTER = 3.5%

OVERSHOOT = 3.0%

AMSU-A2

S/N 105



AE26002/2D

PARA: 3.4.5.5 Step 29



X=4.451 S     $\Delta X=33.98\text{mS}$     Y=13.8145     $\Delta Y=16.97\text{mV}$   
Y<sub>a</sub>=13.8033     $\Delta Y_a=371.4\text{mV}$

CAP TIM BUF  
14.2

100  
m  
/Div

Real

V

JITTER = 2.5%  
OVERSHOOT = 2.3%

13.4

FxdXY 4.4

Sec

Amsa-A2  
S/N 105

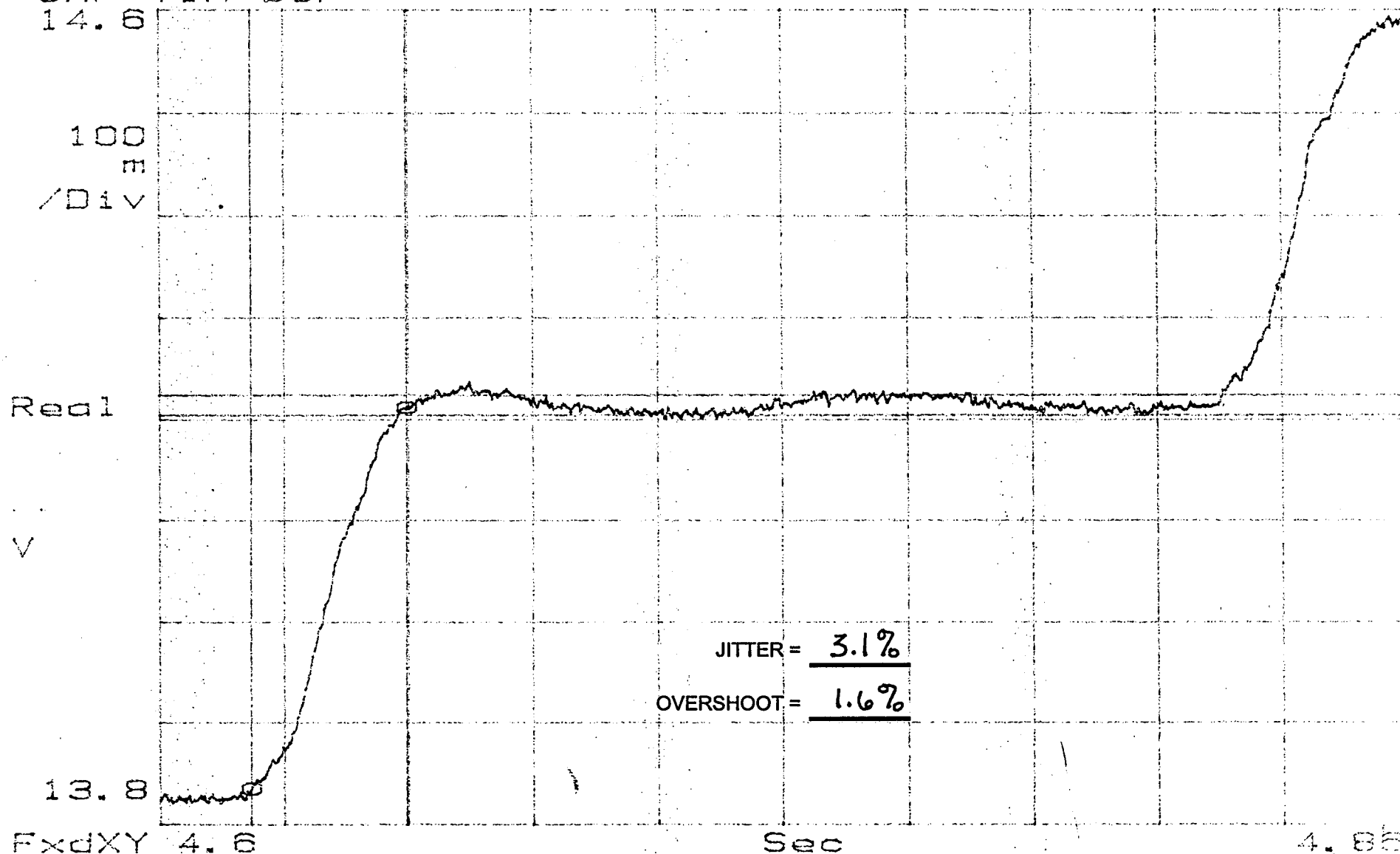
7A  
268

MAY 28 '98

AE26002/2D  
PARA: 3.4.5.5 Step 30

X=4.652 S       $\Delta X=32.42\text{mS}$       Y=14.2042       $\Delta Y=20.36\text{mV}$   
Y<sub>a</sub>=14.191       $\Delta Y_a=376.3\text{mV}$

CAP TIM BUF  
14.6



FxdXY 4.6

AMSU-AZ

S/N 105 (7A 268) MAY 28 '98

AE26002/2D  
PARA: 3.4.5.5 Step 31

SCEN 73  
FILE NAME: 7AP FS1

X=4.854 S    ΔX=32.42ms    Y=14.5724    ΔY=20.85mV  
Y0=14.5607    ΔY0=358.4mV

CAP TIM BUF

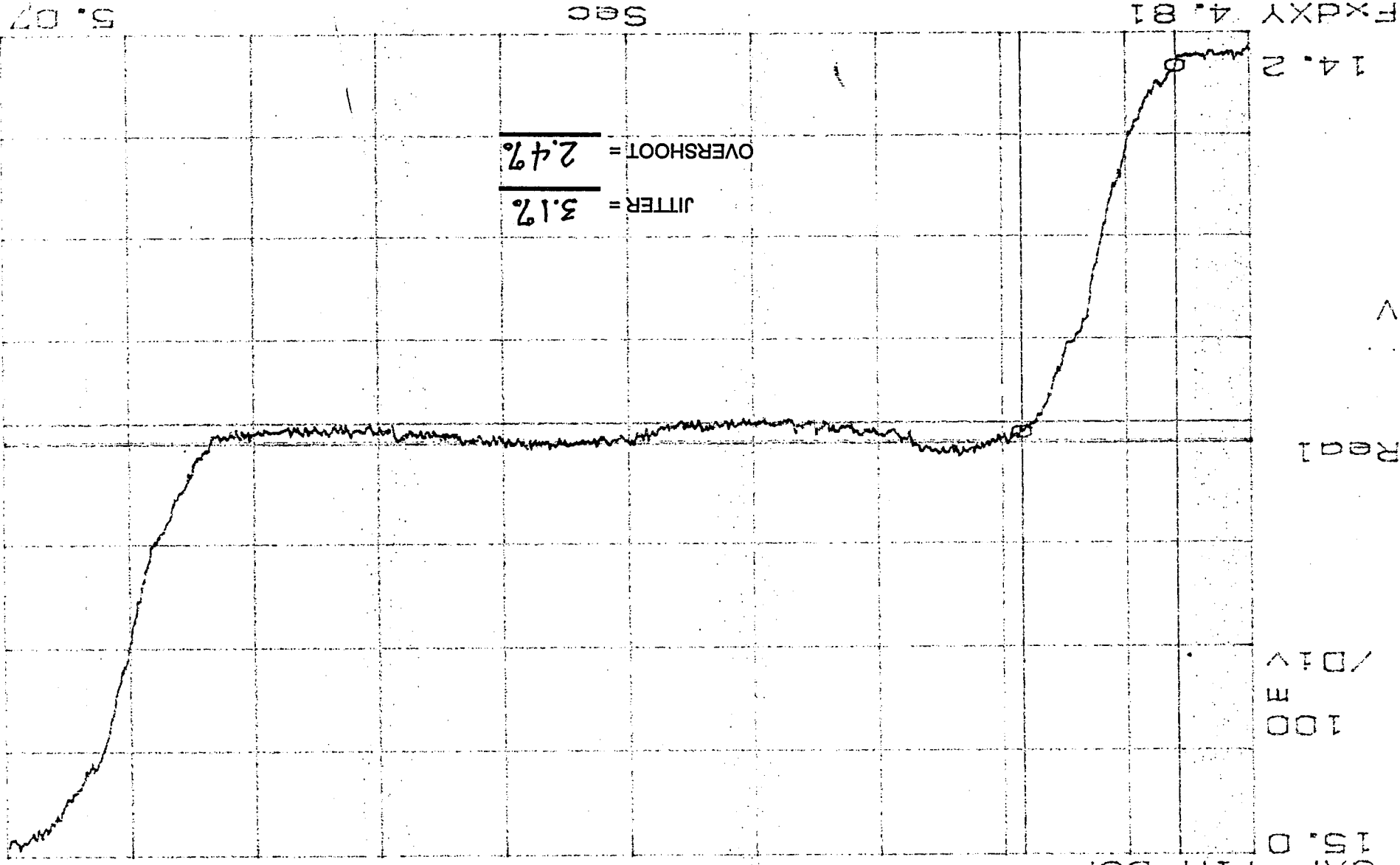
15.0

100

/DIV  
m

Real

V



AE26002/2D  
PARA: 3.4.5 Step 32

MSU-AZ  
S/A 105  
268  
MAY 28 98

SCEN 76  
FILE NAME: TAP FS1

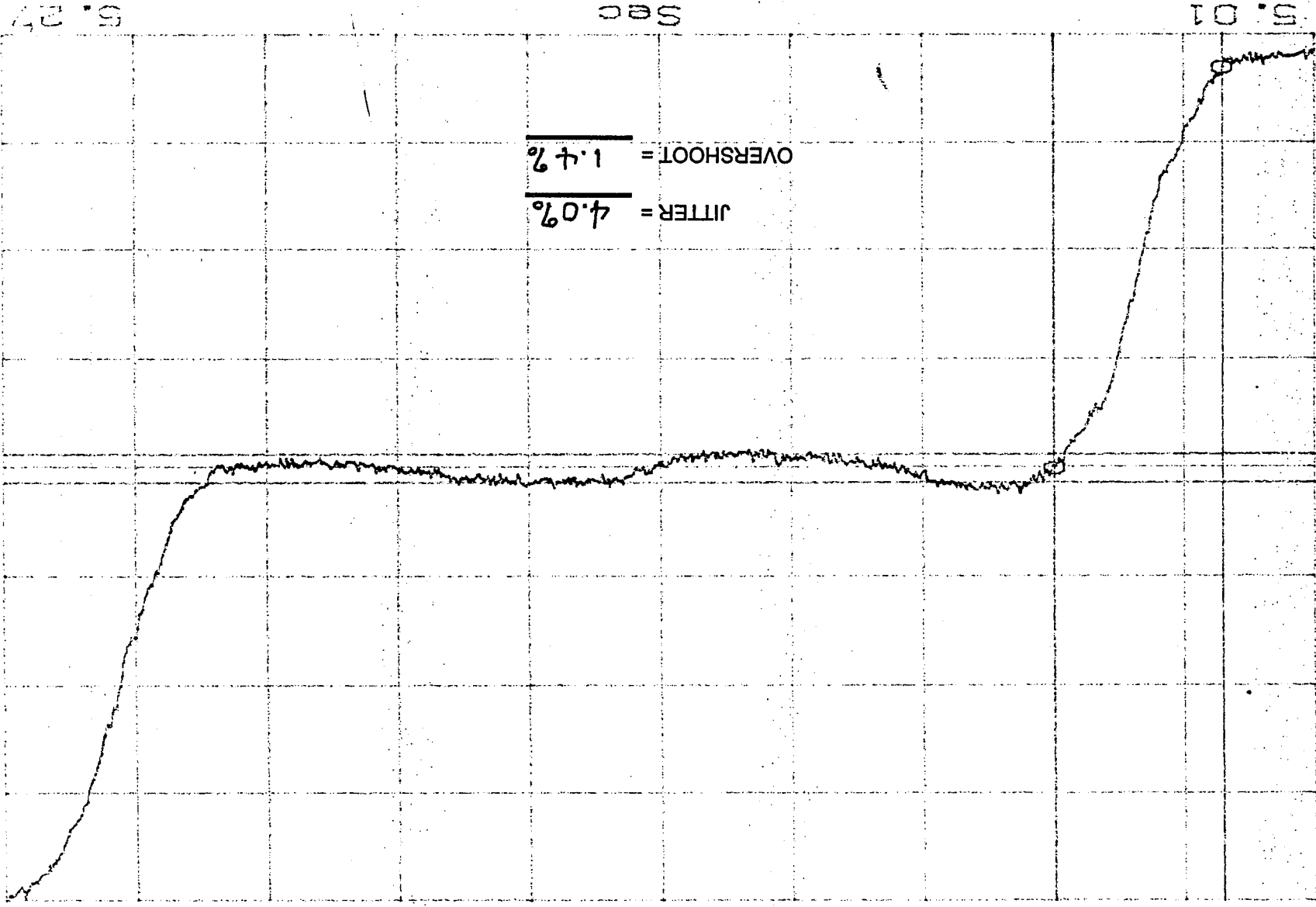
X=5.058 S ΔX=33.59ms Y=14.9545  
Yd=14.9402 ΔYd=369.8mV  
CAP TIM BUF

15.3

100

/DIV  
m

Reg1



Sec

FXDXY 5.01

14.5

AE26002/2D  
PARA: 3.4.5.5 Step 33

7A  
268

MSU-AZ  
S/N105

SCEN 71  
FILE NAME: 7AP FS1

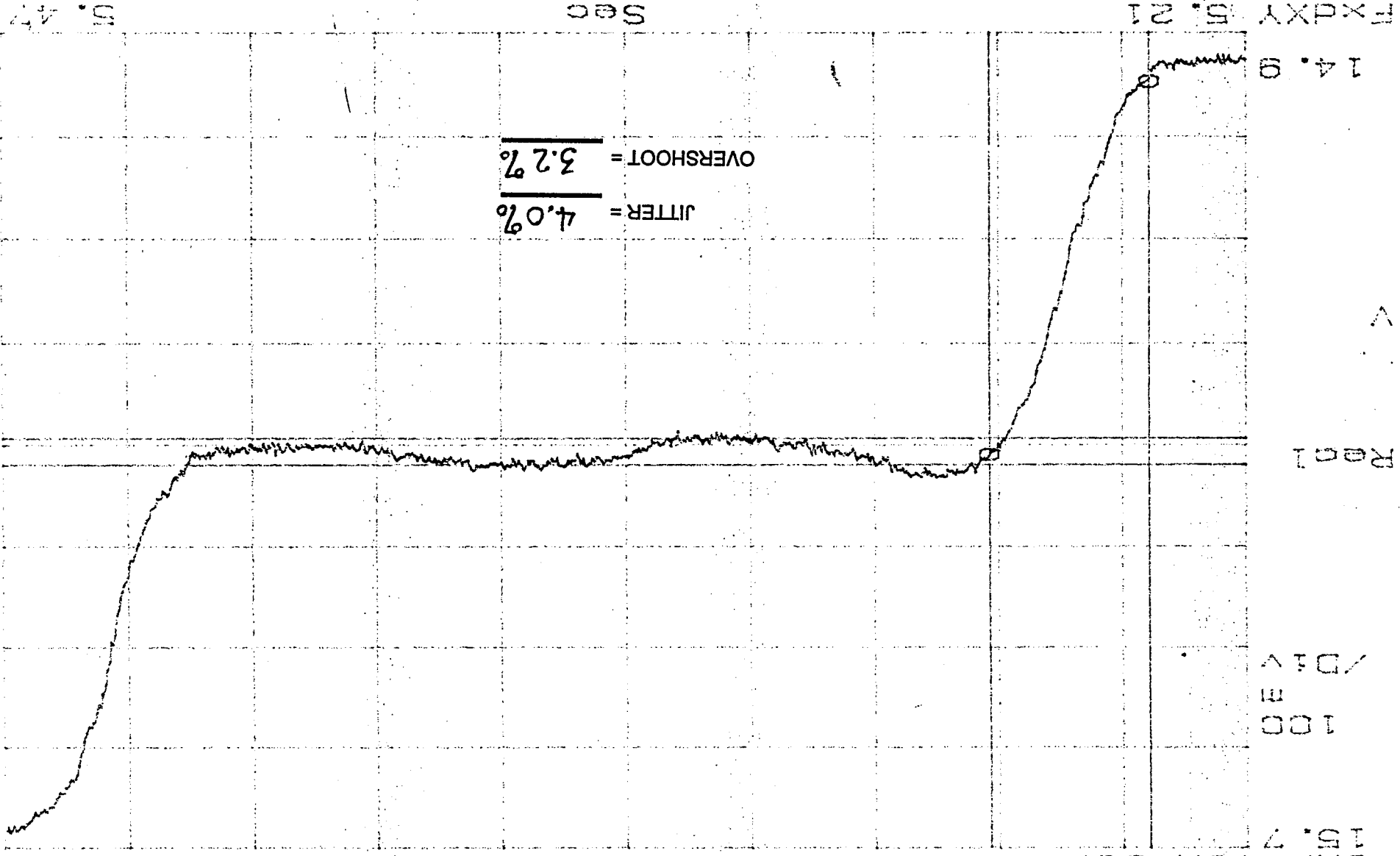
X=5.261 S    ΔX=33.59ms    Y=15.3294    ΔY=26.67mV  
Y0=15.3181    ΔY0=361.7mV

CAP TIM BUF 15.7

100  
m  
/Div

Rec1

V



Sec

FSDXV 5.21

14.9

AE26002/2D  
PARA: 3.4.5.5 Step 34

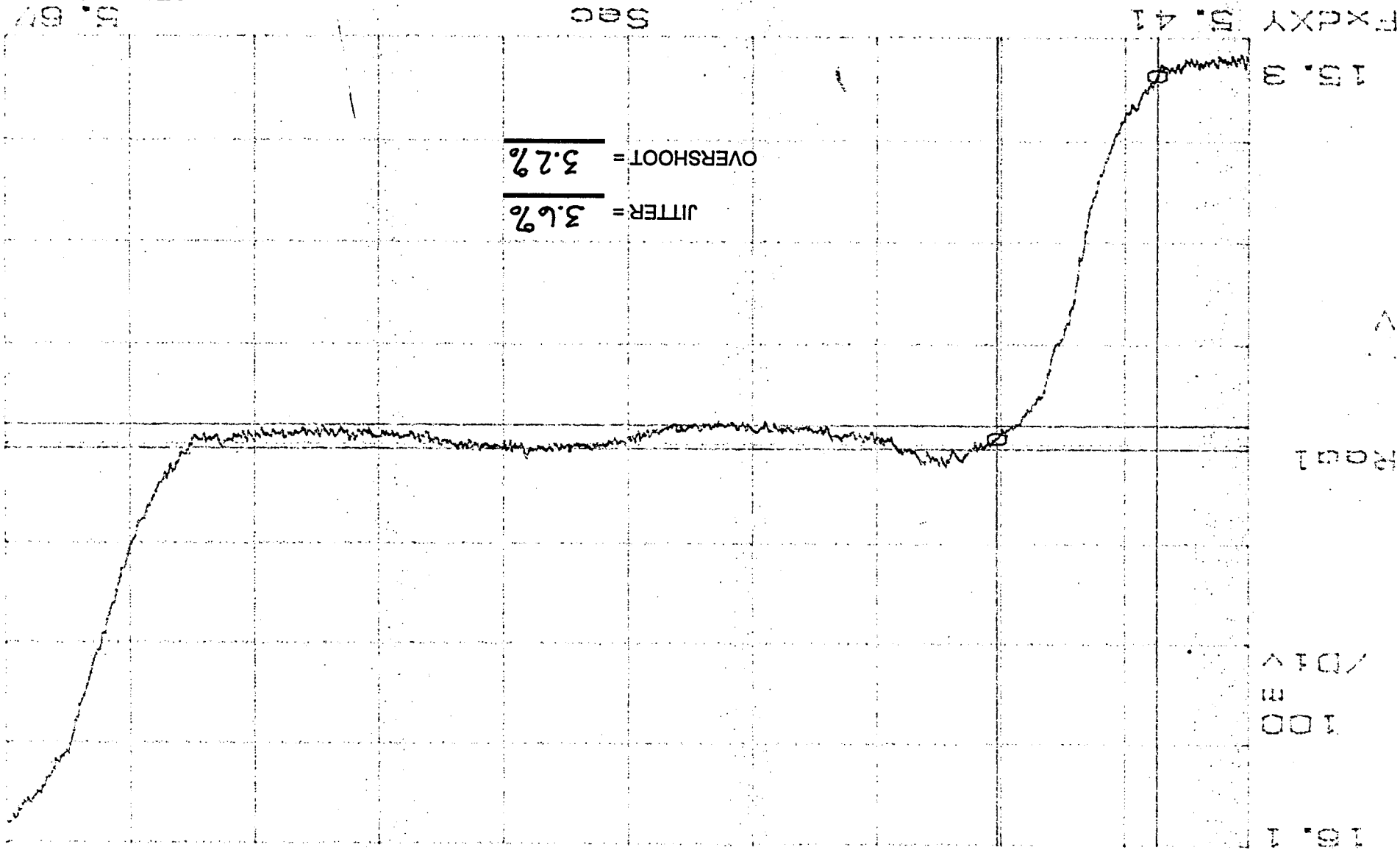
AMSU-A2  
5/N 105  
7A  
268  
MAY 28 98

B2

SCEN 68  
FILE NAME: 7AP FS1

X=5.462 S  
Y=15.683  
CAP TIM BUF  
Y=15.6948  
 $\Delta Y = 24.24 \text{ mV}$

$\Delta X = 33.59 \text{ ms}$   
 $\Delta Y = 356.8 \text{ mV}$



AE26002/2D  
PARA: 3.4.5.5 Step 35

7A  
268  
S/N 105  
ASU-42  
FXXXY 5.41  
15.3

X=5.668 S    ΔX=37.11ms    Y=16.075    ΔY=27.15mV

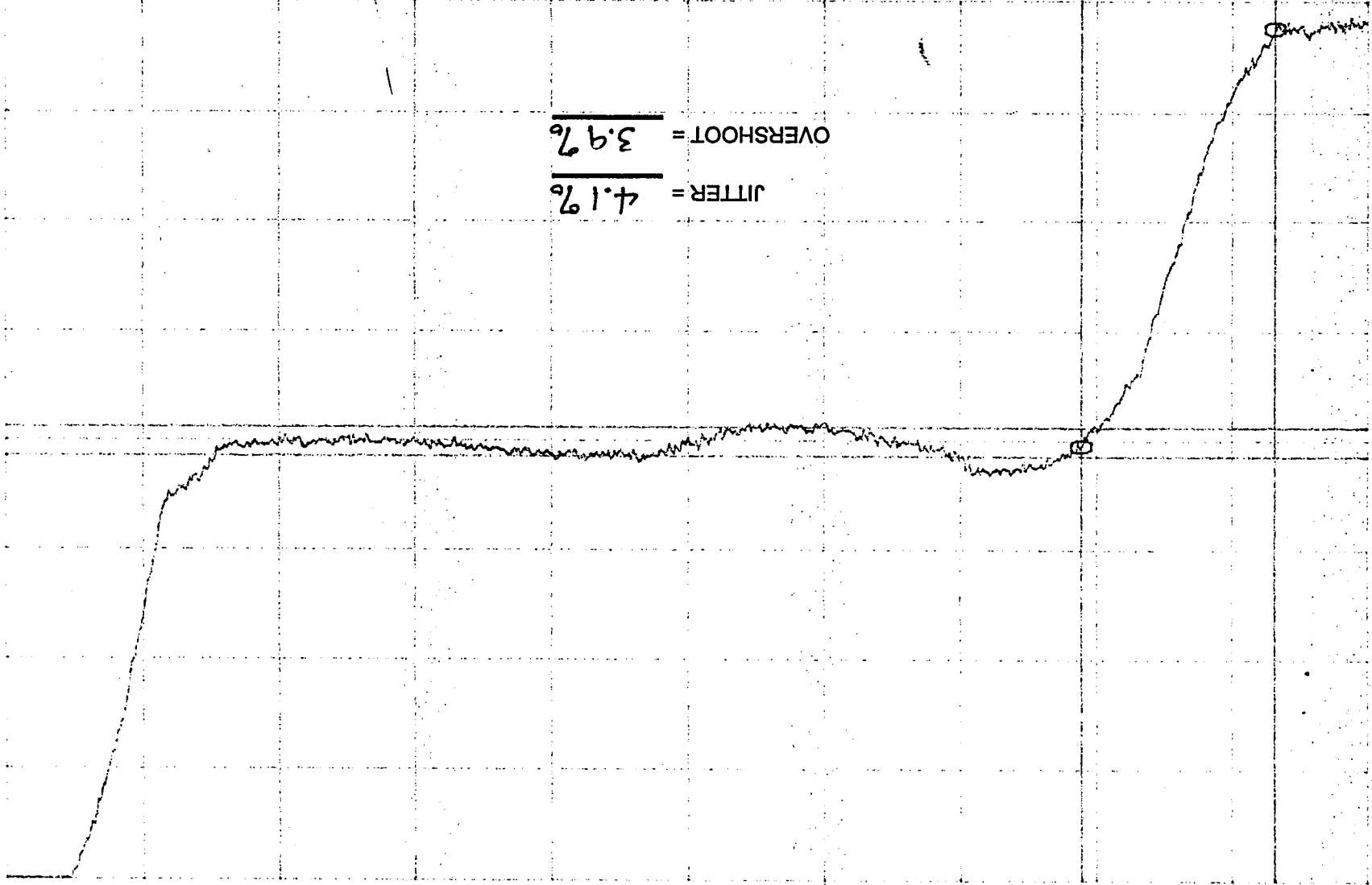
Y0=16.0642

CAP TIM BUF 16.5

100

/DIV

Reg1



FXY 5.61

15.7

MSU-A2  
SN 105  
74  
268  
MAY 28 '88

AE26002/2D  
PARA: 3.4.5.5 Step 36

SCEN 30  
FILE NAME: TAP FS1

X=5.868 S  
Y=16.4939  
ΔX=33.2ms  
Y=16.503  
ΔY=18.91mV

CAP TIM BUF  
16.8

100  
m  
/DIV

Reg1

V

16.0

F\*PKY 5.82

268

AMSU-A2  
S/N 105

MAR 88 98

JITTER = 2.8%  
OVERSHOOT = 3.5%

S90

AE26002/2D  
PARA: 3.4.5.5 Step 37

6.08

739



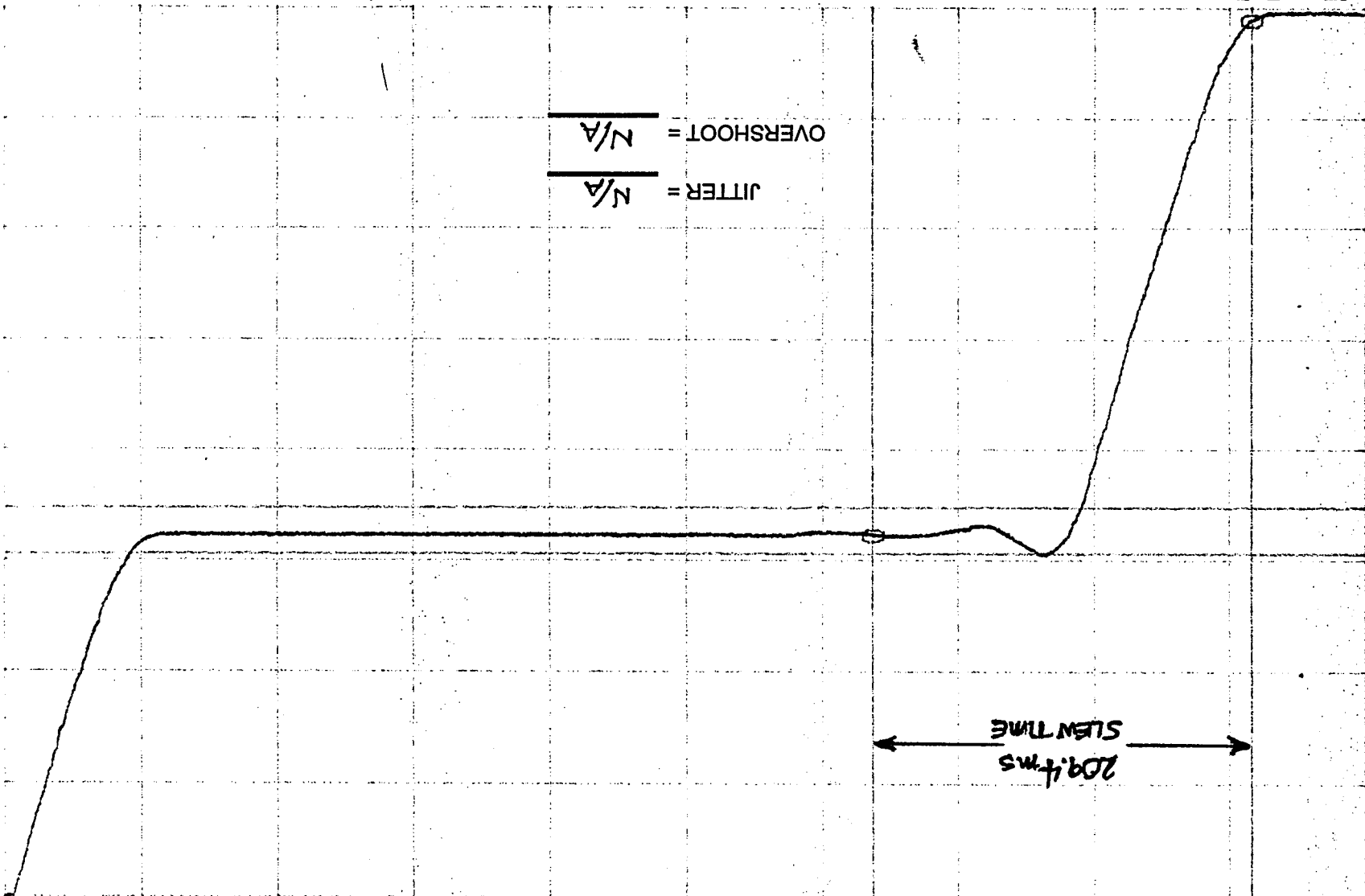
X=6.252 S ΔX=209.4ms ΔY=3.164 V Y=19.8196 ΔY=286.8mV

CAP TIM BUF 21.9

680 /DIV

RODI

V



JITTER = N/A  
OVERSHOOT = N/A

Sec

FXXXY 5.98

18.5

AMSU-A2  
S/N 105

268

AE26002/2D  
PARA: 3.4.5 Step 38

8.72

$\Delta Y = 22.0 \text{ mV}$

$Y = 19.7084$

$\Delta X = 370.7 \text{ ms}$   
 $\Delta Y = 1.625 \text{ mV}$

CAP TIM BUF

19.8

12.5

/DIV

Rec1

V

19.7

FXPXY 5.98

7A  
268  
MAY 28 1988

AMSU-A3  
SIN 105

AE26002/2D  
 PARA: 3.4.5.5 Step 38

Sec

JITTER =  $.11^\circ$  (3.3%)

OVERSHOOT =

$22.0 \text{ mV} = 33\%$   
 $= .11^\circ \text{ jitter}$

370.7ms  
 INTEGRATION  
 PERIOD

X=6.662 S ΔX=400.0ms Y=31.0515 ΔY=882.1mV

Y0=19.8511

ΔY0=11.63 V

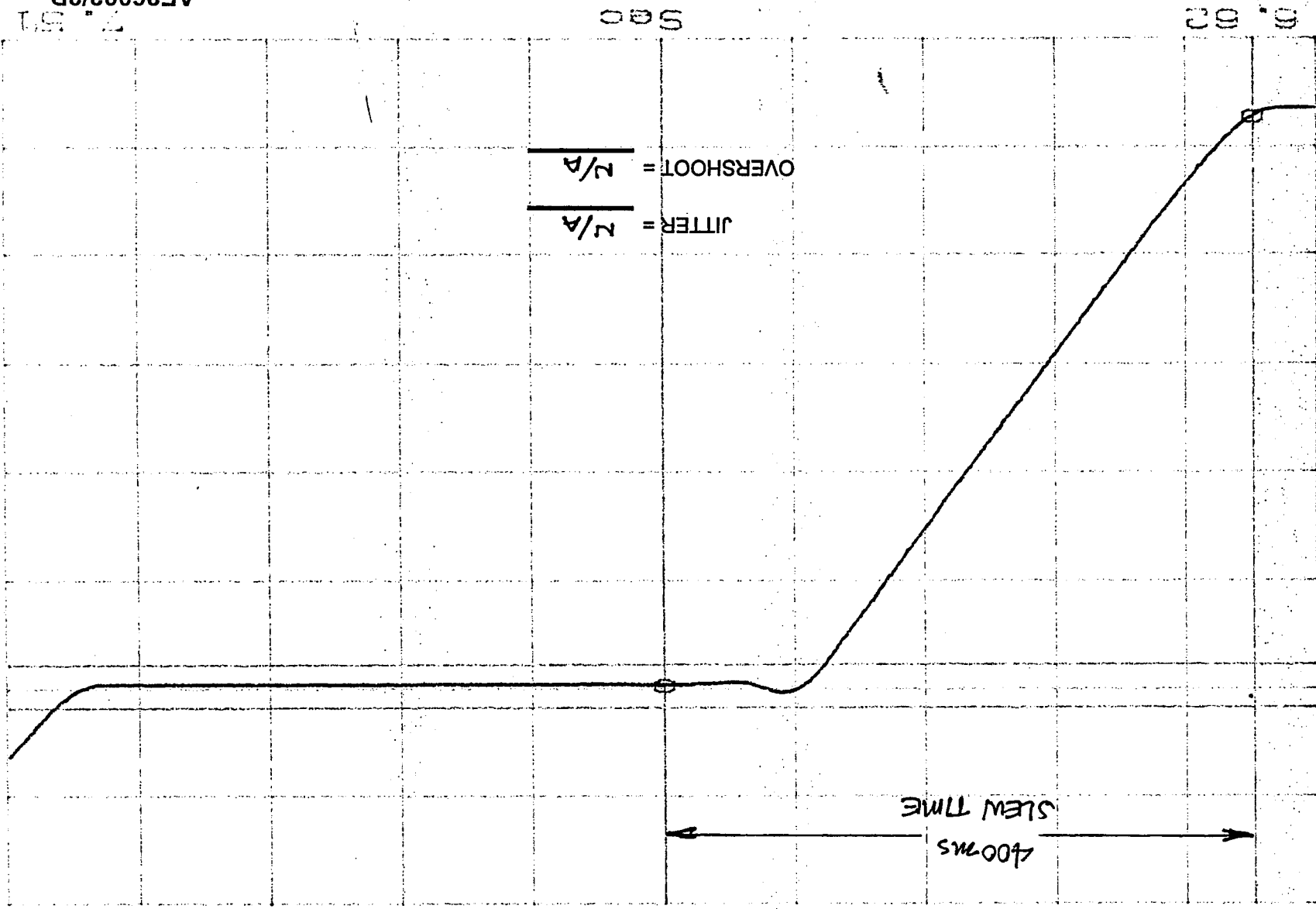
CAP TIM BUF 36.0

2.22

/DIV

Real

V



FXPXY 5.52

560

AE26002/2D  
PARA: 3.4.5.5 Step 39

AMSU-A2  
S/N 105  
268

X=6.662 S ΔX=400.0ms Y=31.4848 ΔY=18.69mV  
Y0=19.851 ΔY0=11.63 V

CAP TIM BUF

31.5

9.22

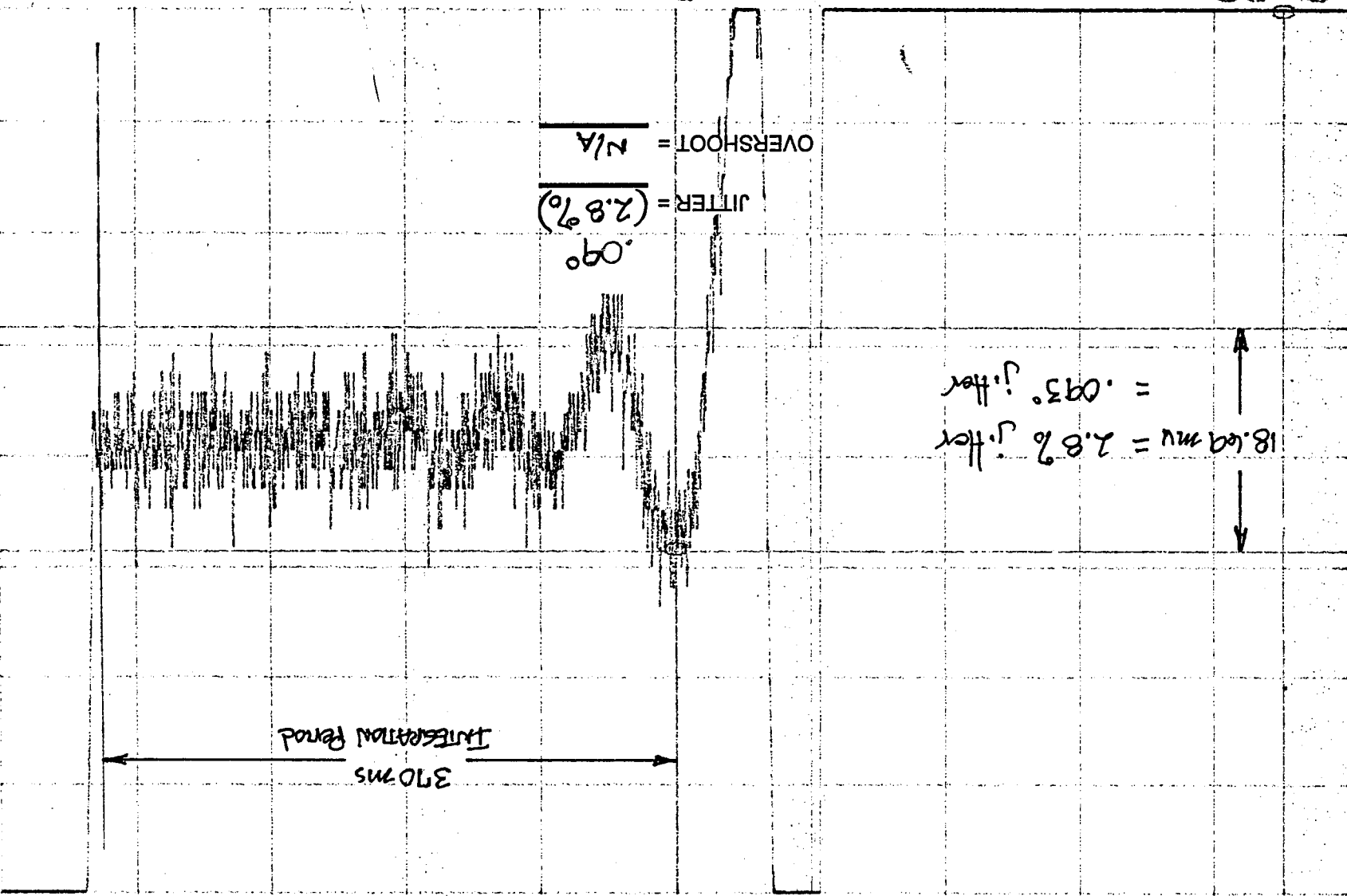
/DIV

Read

V

31.4

Fxdxy 6.62



JITTER = (2.8%)

OVERSHOOT = N/A

18.69mV = 2.8% jitter  
= .093% jitter

Integration Period  
370ms

TEST DATA SHEET 7 (SHEET 1 OF 4)

3.4.4.5: Scan Motion and Jitter Test

5

Test Setup Verified:

*[Signature]*  
Signature

Shop Order No. 484113

Step No.	Description	Requirement	Test Result	Pass/Fail
7	--	Stepping Slewing <8 sec period per Figure 8	<8sec	PASS
9	Scene 1-2 3.33° step	<42 msec rise time per Figure 7	38.7ms	PASS
		< ±5% jitter per Figure 7 < +4% overshoot for 19 msec	3.6% j.t. / 1.6% ovr	PASS
10	Scene 2-3 3.33° step	<42 msec rise time per Figure 7	36.3ms	PASS
		< ±5% jitter per Figure 7 < +4% overshoot for 19 msec	3.4% j.t. / 1.4% ovr	PASS
11	Scene 3-4 3.33° step	<42 msec rise time per Figure 7	32.8ms	PASS
		< ±5% jitter per Figure 7 < +4% overshoot for 19 msec	3.4% j.t. / 1.7% ovr	PASS
12	Scene 4-5 3.33° step	<42 msec rise time per Figure 7	38.3ms	PASS
		< ±5% jitter per Figure 7 < +4% overshoot for 19 msec	2.3% j.t. / 2.4% ovr	PASS
13	Scene 5-6 3.33° step	<42 msec rise time per Figure 7	35.2ms	PASS
		< ±5% jitter per Figure 7 < +4% overshoot for 19 msec	1.6% j.t. / 1.6% ovr	PASS
14	Scene 6-7 3.33° step	<42 msec rise time per Figure 7	37.5ms	PASS
		< ±5% jitter per Figure 7 < +4% overshoot for 19 msec	3.1% j.t. / 2.8% ovr	PASS
15	Scene 7-8 3.33° step	<42 msec rise time per Figure 7	35.9ms	PASS
		< ±5% jitter per Figure 7 < +4% overshoot for 19 msec	3.3% j.t. / 1.4% ovr	PASS
16	Scene 8-9 3.33° step	<42 msec rise time per Figure 7	33.2ms	PASS
		< ±5% jitter per Figure 7 < +4% overshoot for 19 msec	4.2% j.t. / 2.8% ovr	PASS

Figure 7 to 9 change signatures:

TE *[Signature]* 5/18/98

QE *[Signature]*

Quantity *[Signature]*

Pass = P  
Fail = F

7A  
268

B9  
A-9

QC  
223

5/15/98

*[Signature]*

TEST DATA SHEET 7 (SHEET 2 OF 4)  
3.4.4.5: Scan Motion and Jitter Test  
5

Step No.	Description	Requirement	Test Result	Pass/Fail
17	Scene 9-10 3.33° step	<42 msec rise time per Figure 7-9	33.2 ms	PASS
		< ±5% jitter per Figure 7-9 < +4% overshoot for 19 msec	3.2% jit / .8% ovr	PASS
18	Scene 10-11 3.33° step	<42 msec rise time per Figure 7-9	36.3 ms	PASS
		< ±5% jitter per Figure 7-9 < +4% overshoot for 19 msec	3.3% jit / 1.0% ovr	PASS
19	Scene 11-12 3.33° step	<42 msec rise time per Figure 7-9	33.6 ms	PASS
		< ±5% jitter per Figure 7-9 < +4% overshoot for 19 msec	2.9% jit / 1.8% ovr	PASS
20	Scene 12-13 3.33° step	<42 msec rise time per Figure 7-9	32.8 ms	PASS
		< ±5% jitter per Figure 7-9 < +4% overshoot for 19 msec	3.6% jit / 2.9% ovr	PASS
21	Scene 13-14 3.33° step	<42 msec rise time per Figure 7-9	37.9 ms	PASS
		< ±5% jitter per Figure 7-9 < +4% overshoot for 19 msec	2.5% jit / 2.5% ovr	PASS
22	Scene 14-15 3.33° step	<42 msec rise time per Figure 7-9	33.2 ms	PASS
		< ±5% jitter per Figure 7-9 < +4% overshoot for 19 msec	4.4% jit / 2.3% ovr	PASS
23	Scene 15-16 3.33° step	<42 msec rise time per Figure 7-9	37.5 ms	PASS
		< ±5% jitter per Figure 7-9 < +4% overshoot for 19 msec	3.4% jit / 2.3% ovr	PASS
24	Scene 16-17 3.33° step	<42 msec rise time per Figure 7-9	33.2 ms	PASS
		< ±5% jitter per Figure 7-9 < +4% overshoot for 19 msec	3.3% jit / 2.0% ovr	PASS

Figure 7 to 9 change Signatures:

TE

QE

QE

Assurance

Pass = P  
Fail = F



QC  
223

B  
A-10

5/15/98  
Don Higgin

TEST DATA SHEET 7 (SHEET 3 OF 4)  
3.4.4.5: Scan Motion and Jitter Test  
5

Step No.	Description	Requirement	Test Result	Pass/Fail
25	Scene 17-18 3.33° step	<42 msec rise time per Figure 7 9	33.6 ms	PASS
		< ±5% jitter per Figure 7 9 < +4% overshoot for 19 msec	4.8%jit / 2.6%ovr	PASS
26	Scene 18-19 3.33° step	<42 msec rise time per Figure 7 9	32.4 ms	PASS
		< ±5% jitter per Figure 7 9 < +4% overshoot for 19 msec	2.6%jit / 1.1%ovr	PASS
27	Scene 19-20 3.33° step	<42 msec rise time per Figure 7 9	31.25 ms	PASS
		< ±5% jitter per Figure 7 9 < +4% overshoot for 19 msec	4.3%jit / 2.3%ovr	PASS
28	Scene 20-21 3.33° step	<42 msec rise time per Figure 7 9	35 s	PASS
		< ±5% jitter per Figure 7 9 < +4% overshoot for 19 msec	3.1%jit / 8.0%ovr	PASS
29	Scene 21-22 3.33° step	<42 msec rise time per Figure 7 9	32.8 ms	PASS
		< ±5% jitter per Figure 7 9 < +4% overshoot for 19 msec	3.5%jit / 3.0%ovr	PASS
30	Scene 22-23 3.33° step	<42 msec rise time per Figure 7 9	34 ms	PASS
		< ±5% jitter per Figure 7 9 < +4% overshoot for 19 msec	2.5%jit / 2.3%ovr	PASS
31	Scene 23-24 3.33° step	<42 msec rise time per Figure 7 9	32.4 ms	PASS
		< ±5% jitter per Figure 7 9 < +4% overshoot for 19 msec	3.1%jit / 1.6%ovr	PASS
32	Scene 24-25 3.33° step	<42 msec rise time per Figure 7 9	32.4 ms	PASS
		< ±5% jitter per Figure 7 9 < +4% overshoot for 19 msec	3.1%jit / 2.4%ovr	PASS

Figure 7 to 9 Change Signatures:

TE Tom Anglin 5/18/98

QE

QE

QUALITY

ASSURANCE

hyn

Pass = P  
Fail = F

5/19/98

BK-11 2C 223 5/15/98  
Tom Anglin

TEST DATA SHEET 7 (SHEET 4 OF 4)

3.4.4.5: Scan Motion and Jitter Test

5

Step No.	Description	Requirement	Test Result	Pass/Fail
33	Scene 25-26 3.33° step	<42 msec rise time per Figure 7 9	33.6 ms	PASS
		< ±5% jitter per Figure 7 9	4.0% jit / 1.4% ovr	PASS
		< +4% overshoot for 19 msec		
34	Scene 26-27 3.33° step	<42 msec rise time per Figure 7 9	33.6 ms	PASS
		< ±5% jitter per Figure 7 9	4.0% jit / 3.2% ovr	PASS
		< +4% overshoot for 19 msec		
35	Scene 27-28 3.33° step	<42 msec rise time per Figure 7 9	33.6 ms	PASS
		< ±5% jitter per Figure 7 9	3.6% jit / 3.2% ovr	PASS
		< +4% overshoot for 19 msec		
36	Scene 28-29 3.33° step	<42 msec rise time per Figure 7 9	37.1 ms	PASS
		< ±5% jitter per Figure 7 9	4.1% jit / 3.9% ovr	PASS
		< +4% overshoot for 19 msec		
37	Scene 29-30 3.33° step	<42 msec rise time per Figure 7 9	33.2 ms	PASS
		< ±5% jitter per Figure 7 9	2.8% jit / 3.5% ovr	PASS
		< +4% overshoot for 19 msec		
38	Scene 30 Cold Cal 35.0° slew	<0.21 sec slew time per Figure 10 12	<.210 sec	PASS
		< ±0.165° jitter per Figure 10 13	.110° jit	PASS
39	Cold Cal - Warm Cal 96.67° slew	<0.40 sec slew time per Figure 11 14	<.400 sec	PASS
		< ±0.165° jitter per Figure 12 15	.093° jit	PASS

Figure 7, 10, 11, and 12 to 12, 13, 14, & 15  
change Signatures:

TE Tom Dufin 5/18/98

Pass = P  
Fail = F

Quality Assurance  
QA Angie

Unit: METSAT AMSU-AZ

Test Engineer: Tom Dufin

Serial No.: 105

Quality Assurance: Angie

Date: 5/19/98

Customer Representative: \_\_\_\_\_

QC  
223

B-12

5/15/98  
Tom Dufin



X=6.9902 Sec  
Y=59.4201mV

CAP TIM BUF

150 H

20.0 H

/Div

Real

V

-10.0 m\*

FxdY 0.0

Sec

8.0

C1

METSAT AMSU-A2 S/N 105

AE 2600Z/20 para 3.4.5.6

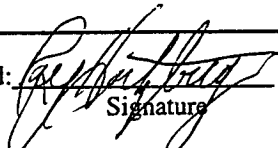
Peak Current =  $93.85 \times \left( \frac{200 \text{ mV}}{10 \text{ mV}} \right) = 1.877 \text{ A}$

28V BUS PEAK CURRENT TEST SETUP

F.I.F. 1.0 PI B C

7A  
288  
100

TEST DATA SHEET 8  
3.4.4.6: Pulse Load Bus Current

Test Setup Verified: 

Signature

Shop Order No. 484113

3.4.4.6: 28V Bus Peak Current and Rise Time Test

Step No.	Requirement	Test Result	Pass/Fail
4	< 2 A peak any place in the scan	1.877 A	PASS
5	> 70 $\mu$ sec rise time, 3.33° step	1.1 ms	PASS
6	> 70 $\mu$ sec rise time, start of WC slew	TR 1.2 ms 1.6 ms	PASS
6	> 70 $\mu$ sec rise time, end of WC slew	50 ms	PASS

Pass = P  
Fail = F

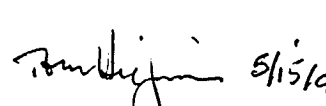
Unit: METEST AMSU-AZ

Serial No.: 105

Test Engineer: 

Quality Assurance:  5/19/98

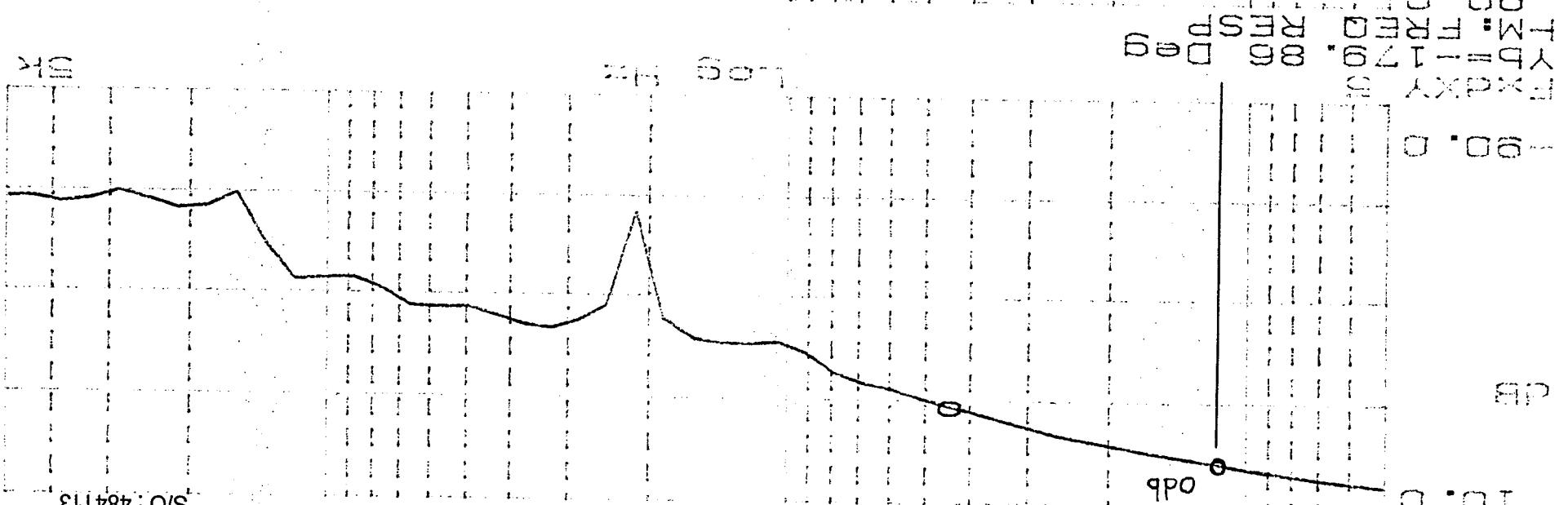
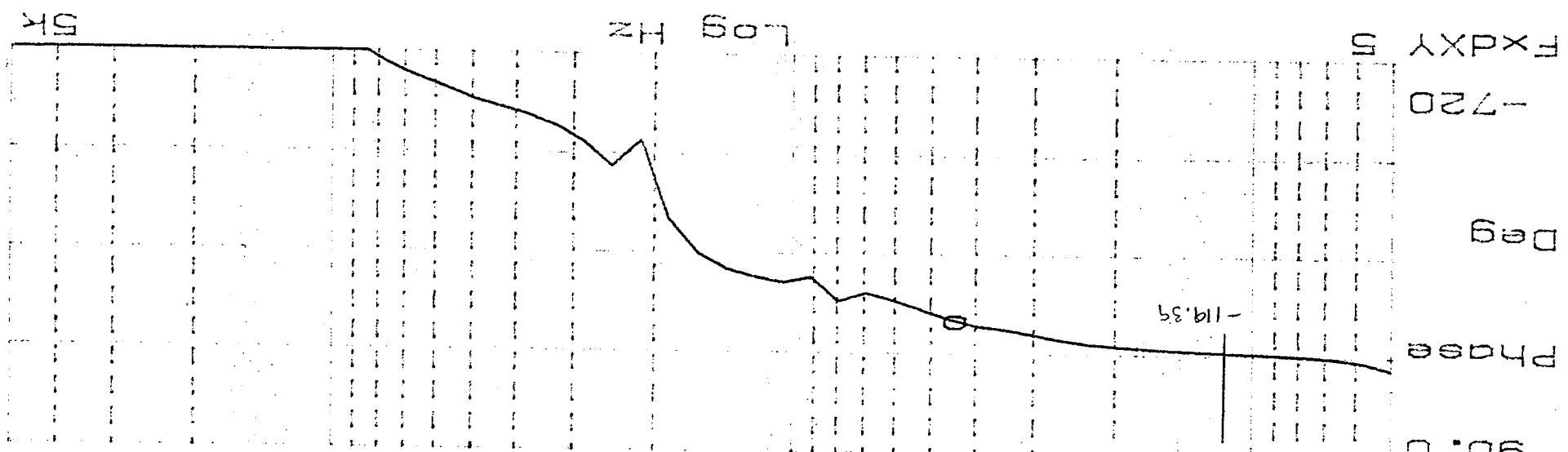
Date: 5/19/98

BA-13  5/15/98

Phase Margin:  $179.86 - 119.39 = 60.47^\circ$   
 Gain Margin:  $-13.5 \text{ dB}$

file: 1162-B11

METSAT AMSU-A2 S/N: 105  
 AE 26002/2D para: 3.4.5.8



TE Name: *[Signature]*  
 Date: 5/21/98  
 S/O: 484113

$X = 44.434 \text{ Hz}$   
 $Y = -13.521 \text{ dB}$   
 H-M: FREQ RESP  
 10.0

X=44.434 Hz  
Y<sub>a</sub>=-13.511 dB

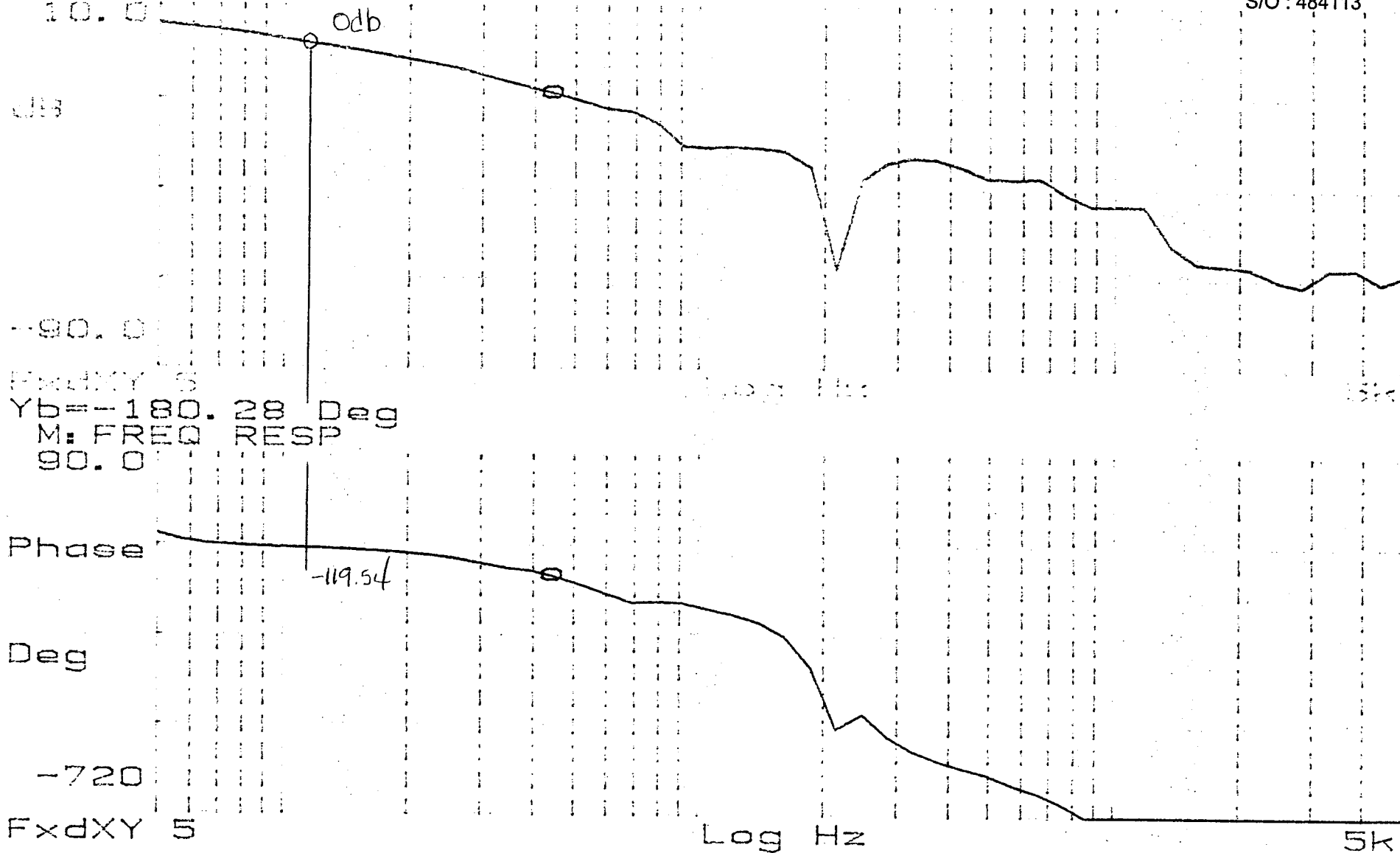
M: FREQ RESP  
10.0

TE Name:

QE Name:

Date: 5/21/98

S/O: 484113



Phase Margin:  $180.28 - 119.54 = 60.74^\circ$

Gain Margin: -13.5 dB

File: 126P 213

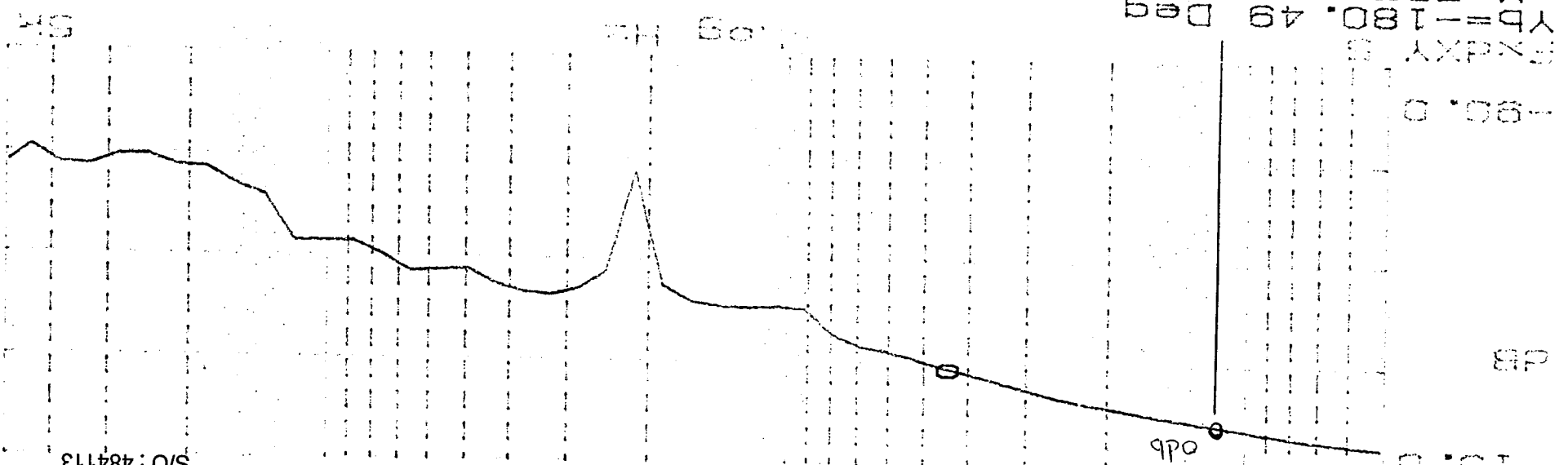
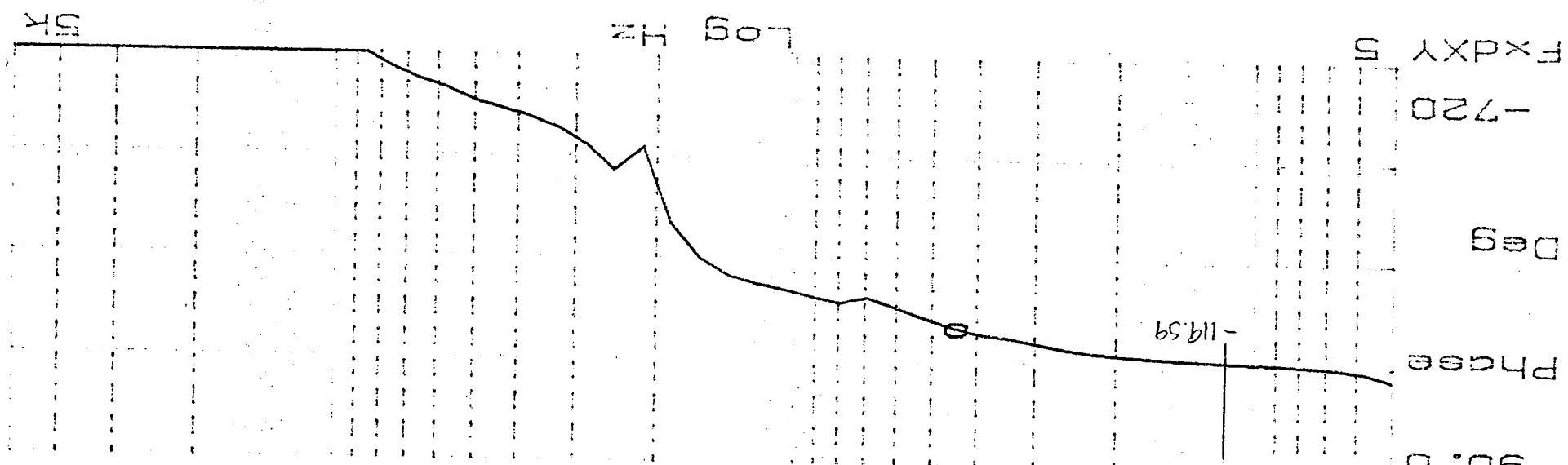
METSAT AMSU-A2 S/N: 105  
AE 26002/2D para: 3.4.5.8

02

Phase Margin:  $180.49 - 119.59 = 60.4^\circ$   
 Gain Margin:  $-13.54\text{ dB}$

File: 126P-313

METSAT AMSU-A2 S/N: 105  
 AE 26002/2D para: 3.4.5.8



TE Name: 18m 484113  
 OE Name: 5/21/15  
 Date: 5/21/15  
 S/O: 484113

TEST DATA SHEET 9  
3.4.4.8 : Gain/Phase Margin Test  
5

Test Setup Verified: *[Signature]*

Signature

Shop Order No. 48413

Temperature: 71.3 °F °C

3.4.4.8 Step 12: Gain/Phase Margin Test

Requirement	Test Result		Pass/Fail
12 dB minimum	1	-13.5	PASS
	2	-13.5	
	3	-13.5	
	<del>4</del>	—	
	<del>5</del>	—	
25 degrees minimum	1	60.5	PASS
	2	60.7	
	3	60.9	
	<del>4</del>	—	
	<del>5</del>	—	

Pass = P  
Fail = F

Unit: METSAT AMBA-AZ

Serial No.: 105

Date: 5/21/98

Test Engineer: *[Signature]*

Quality Assurance: *[Signature]*

Customer Representative: \_\_\_\_\_

*[Signature]*  
B-X-14 *[Signature]* 5/15/98

X=76.56 Hz  
Y0=-27.579 DBVrms

POWER SPEC2 3AVG 0%OVP UnitF

10.0

10.0

/DIV

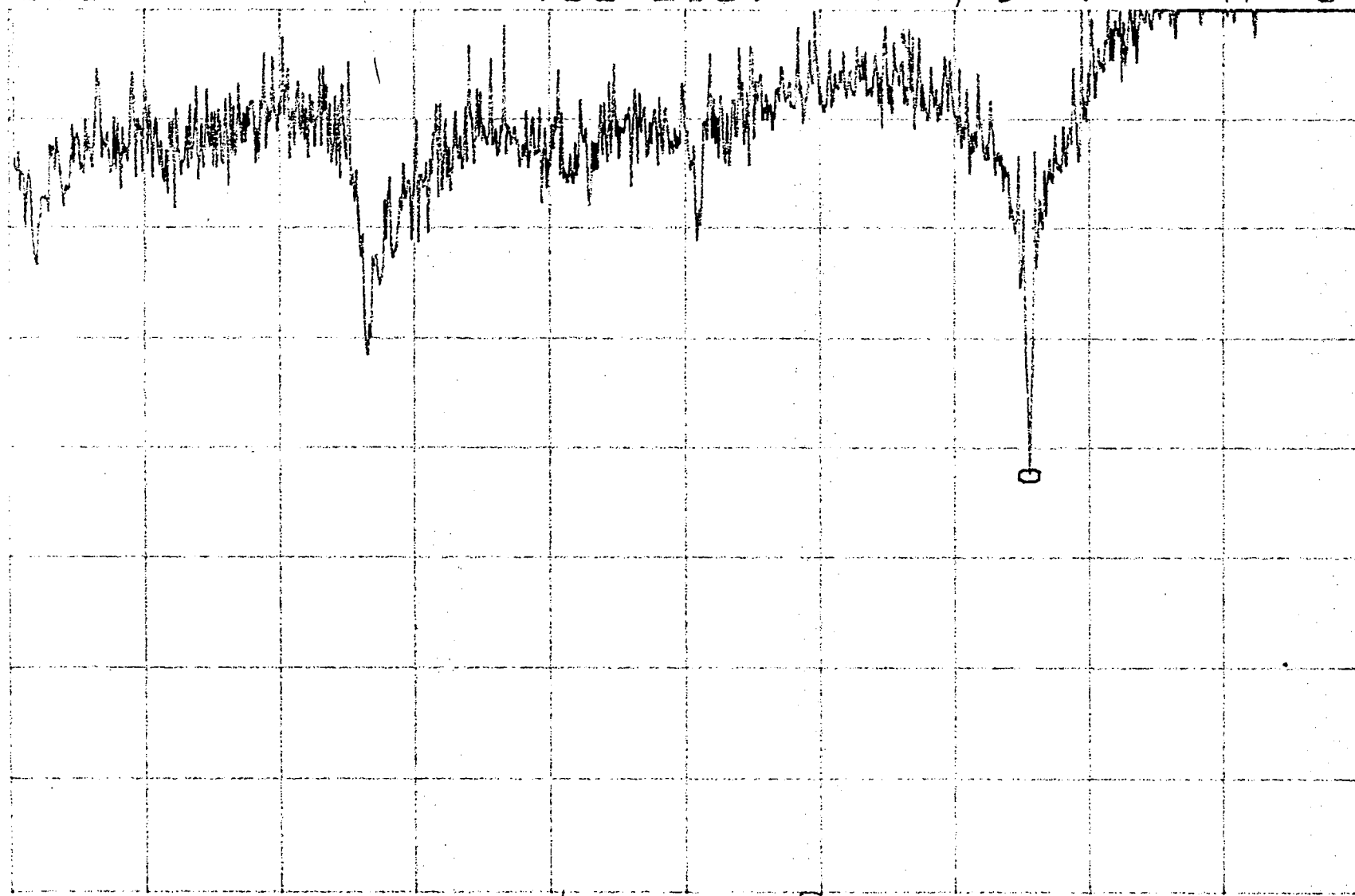
dB

Vrms

V2

-70.0

FXY 0 Hz



Power Spectrum 1 120F PS1

312

S/O: 484113

S/N: 105

P/N: 1331200-2-11

A2 345  
Rpt=36.63KJ  
R58=38.58KJ  
R58=18.2KJ

Gain Margin=8.984dB

Test Eng:

Quality:

Signature  
MAY 28 '98

Date: 5-26-98

TEST DATA SHEET 10  
3.4.4.9: Operational Gain Margin Test

Test Setup Verified: [Signature]

Signature

Shop Order No. 484113

Temperature: 71.2 °C

3.4.4.9: Operation Gain Margin Test

Step No.	Requirement	Test Result		Pass/Fail
11	R58 Resistance (Kohms)	1	38.58 K $\Omega$	P
	Test Pot Resistance (Kohms)	2	41.2 K $\Omega$	P
		3	42.88 K $\Omega$	P
12	Oscillation Frequency (Hz)	1	227.73 Hz	P
		2	228.52 Hz	P
		3	228.52 Hz	P
16	Gain Margin, 9 dB minimum	1	9.277 dB	P
		2	9.6563 dB	P
		3	9.8908 dB	P

Pass = P  
Fail = F

Unit: METSAT AMSU-A2

Serial No.: 105

Test Engineer: [Signature]


Quality Assurance: [Signature]

Date: 5-26-98

B  
X-15

[Signature] 5/15/98 Wynn



 <b>NASA</b> National Aeronautics and Space Administration		Report Documentation Page	
1. Report No. ---	2. Government Accession No. ---	3. Recipient's Catalog No. ---	
4. Title and Subtitle  Integrated Advanced Microwave Sounding Unit-A (AMSU-A), Performance Verification Report		5. Report Date July 1998	
		6. Performing Organization Code ---	
7. Author(s)  A. Nieto		8. Performing Organization Report No. 11190	
		10. Work Unit No. ---	
9. Performing Organization Name and Address Aerojet 1100 W. Hollyvale Azusa, CA 91702		11. Contract or Grant No. NAS 5-32314	
		13. Type of Report and Period Covered Final	
12. Sponsoring Agency Name and Address NASA Goddard Space Flight Center Greenbelt, Maryland 20771		14. Sponsoring Agency Code ---	
15. Supplementary Notes  ---			
16. ABSTRACT (Maximum 200 words )  This is the Performance Verification Report, METSAT AMSU-A2 Antenna Drive Subassy, P/N 1331200-2, S/N 105 for the Integrated Advanced Microwave Sounding Unit-A (AMSU-A).			
17. Key Words (Suggested by Author(s))  EOS Microwave System		18. Distribution Statement  Unclassified --- Unlimited	
19. Security Classif. (of this report)  Unclassified	20. Security Classif. (of this page)  Unclassified	21. No. of pages	22. Price  ---

NASA FORM 1626 OCT 86

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6. AUTHOR(S) A. Nieto				
7. PERFORMING ORGANIZATION NAME(S) AND ADDRESS(ES) Aerojet 1100 W. Hollyvale Azusa, CA 91702			8. PERFORMING ORGANIZATION REPORT NUMBER  11190 July 1998	
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